# ZL8

**CPU CORE**
SENTECH
SC451ITSTR
Page:23

**SYSTEM 3V/5V**
MAXIM
MAX1999 Page:24
+3VPCU
+3V_S5/+3VSUS
+3V
+5VPCU
+5VSUS
+5V

+15V

+1.8VSUS
+1.8V ON
NCP5214

+0.9VSUS
+0.9V ON
NCP5214

+1.5V SENTECH
SC1470

+1.5V_S5
SI9183-AD

+2.5V SENTECH
SC1565

+1.05V SENTECH
SC4215
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MAXIM
MAX8724 Page:26

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ICS954206
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**CELERON-M/PENTIUM-M**
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HOST BUS 533/400MHZ

**NB**
**INTEL**
**ALVISO 915GM**
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DDR-II SODIMM2
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DDR-II

RGB

LVDS

CRT
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LVDS
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(Option)
MINI-PCIE slot
Wireless LAN
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DMI I/F

PCI-E BUS

PCI BUS 33MHZ

USB 2.0

LPC 33MHZ

**SB**
**INTEL**
**ICH6-M**
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ATA 66/100

IDE-ODD
Page: 16

ATA 66/100

HD Audio

TI PCMCIA
PCI1510A ( L-F )
AD17
REQ1# / GNT1#
INTC#
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TYPE II
SLOT
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MINI-PCI
Wireless LAN
AD20
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REALTEK
RTL8100CL
AD24
REQ0# / GNT0#
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ALC260 (ALC883)
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MODEM
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PC97551
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SPEAKER
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LINE OUT
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RJ11
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Keyboard
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FLASH
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FAN
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SYSTEM
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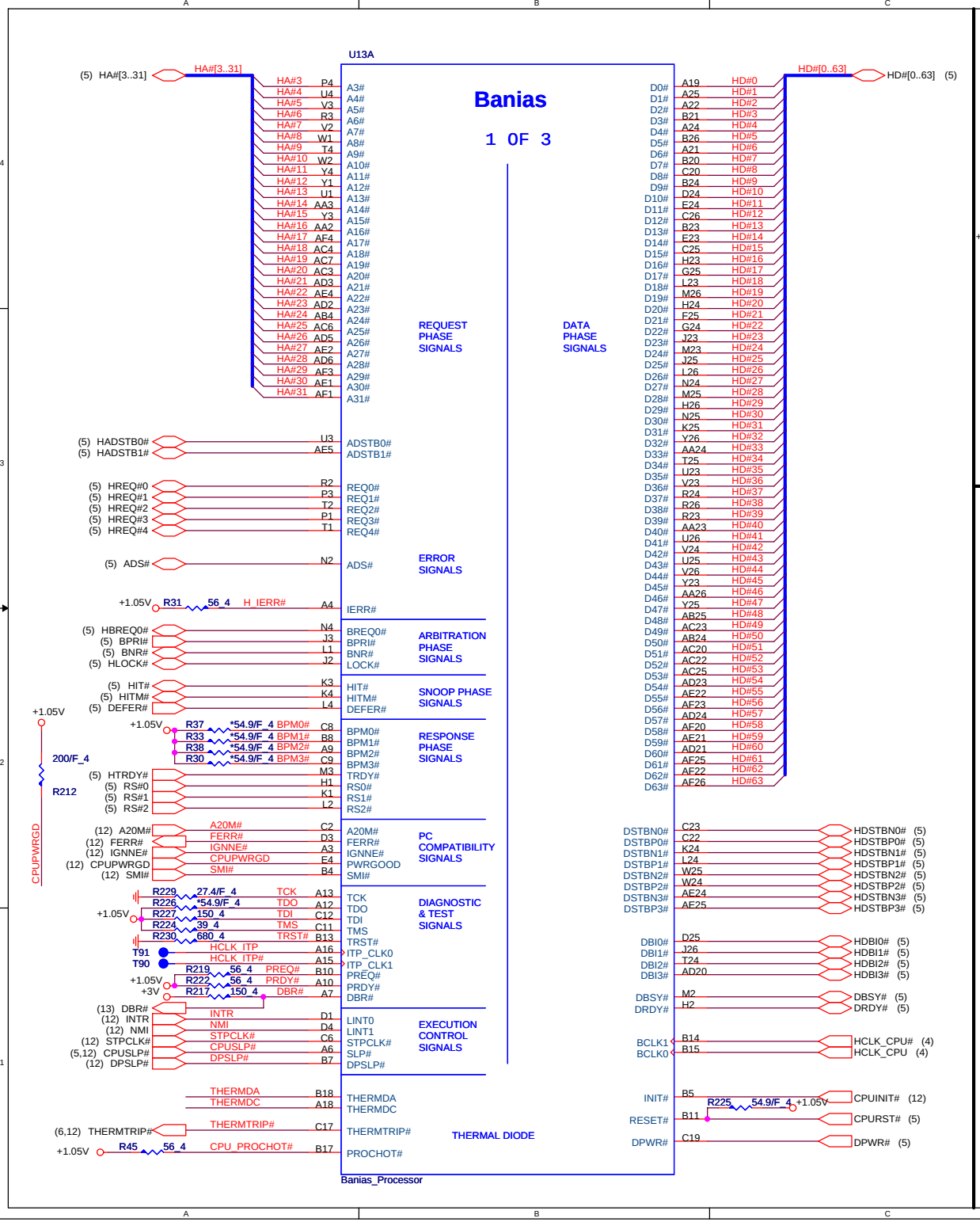
USB2,3,5 Page:16

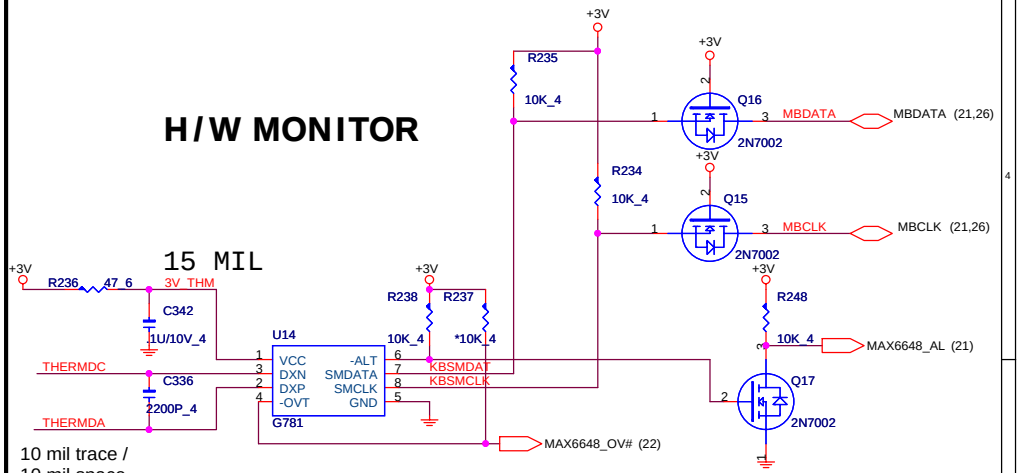
Bluetooth
USB
interface
USB4 Page:16

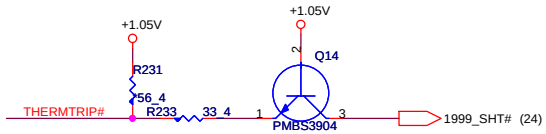
PROJECT : ZL8
Quanta Computer Inc.

| Size | Document Number | Rev |
|---|---|---|
| | BLOCK DIAGRAM | 1A |

Date: Monday, June 13, 2005 Sheet 1 of 26



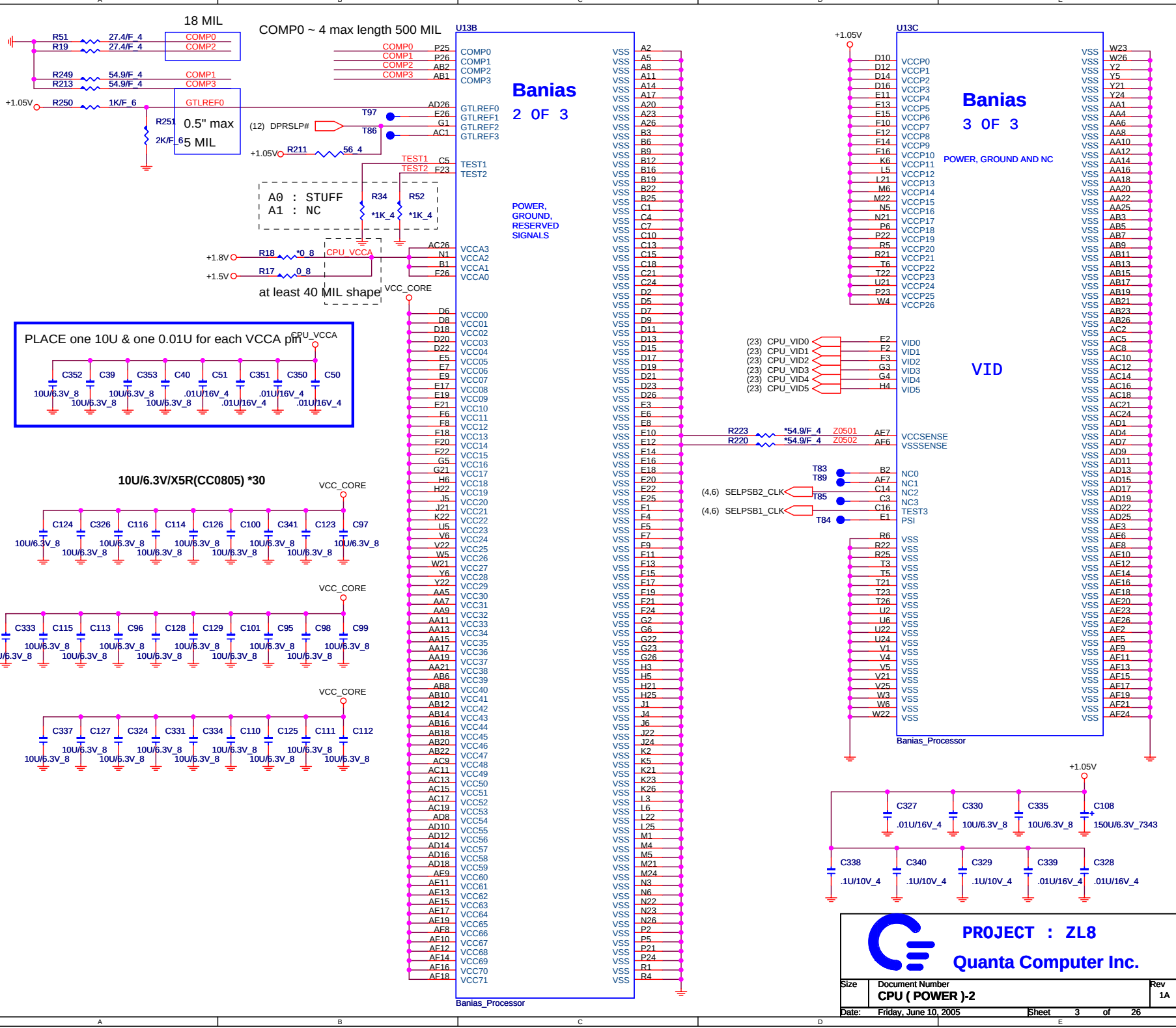




PROJECT : ZL8

Quanta Computer Inc.

| Size | Document Number | Rev |
|---|---|---|
| | CPU ( HOST BUS )-1 | 1A |
| Date: Friday, June 10, 2005 | Sheet 2 of 26 | |


18 MIL
COMP0 ~ 4 max length 500 MIL
R51 27.4/F_4 COMP0
R19 27.4/F_4 COMP2
R249 54.9/F_4 COMP1
R213 54.9/F_4 COMP3
+1.05V R250 1K/F_6 GTLREF0
R251 2K/F_6 0.5" max 5 MIL
(12) DPRSLP#
T97
T86
+1.05V R211 56_4
TEST1
TEST2
A0 : STUFF
A1 : NC
R34 *1K_4
R52 *1K_4
+1.8V R18 *0_8 CPU_VCCA
+1.5V R17 0_8
at least 40 MIL shape
VCC_CORE
PLACE one 10U & one 0.01U for each VCCA pin
CPU_VCCA
C352 C39 C353 C40 C51 C351 C350 C50
10U/6.3V_8 .01U/16V_4
10U/6.3V/X5R(CC0805) *30
VCC_CORE
C124 C326 C116 C114 C126 C100 C341 C123 C97
C333 C115 C113 C96 C128 C129 C101 C95 C98 C99
C337 C127 C324 C331 C334 C110 C125 C111 C112
U13B
Banias
2 OF 3
POWER, GROUND, RESERVED SIGNALS
Banias_Processor
U13C
Banias
3 OF 3
POWER, GROUND AND NC
VID
(23) CPU_VID0
(23) CPU_VID1
(23) CPU_VID2
(23) CPU_VID3
(23) CPU_VID4
(23) CPU_VID5
R223 *54.9/F_4 Z0501
R220 *54.9/F_4 Z0502
VCCSENSE
VSSSENSE
T83
T89
T85
T84
(4,6) SELPSB2_CLK
(4,6) SELPSB1_CLK
NC0
NC1
NC2
NC3
TEST3
PSI
Banias_Processor
+1.05V
C327 .01U/16V_4
C330 10U/6.3V_8
C335 10U/6.3V_8
C108 150U/6.3V_7343
C338 .1U/10V_4
C340 .1U/10V_4
C329 .1U/10V_4
C339 .01U/16V_4
C328 .01U/16V_4
PROJECT : ZL8
Quanta Computer Inc.
Size
Document Number
CPU ( POWER )-2
Rev
1A
Date: Friday, June 10, 2005
Sheet 3 of 26

| FSC | FSB | FSA | CPU | SRC | PCI |
|---|---|---|---|---|---|
| 1 | 0 | 1 | 100 | 100 | 33 |
| 0 | 0 | 1 | 133 | 100 | 33 |
| 0 | 1 | 1 | 166 | 100 | 33 |
| 0 | 1 | 0 | 200 | 100 | 33 |
| 0 | 0 | 0 | 266 | 100 | 33 |
| 1 | 0 | 0 | 333 | 100 | 33 |
| 1 | 1 | 0 | 400 | 100 | 33 |
| 1 | 1 | 1 | RSVD | 100 | 33 |

BOTHAN-A 400

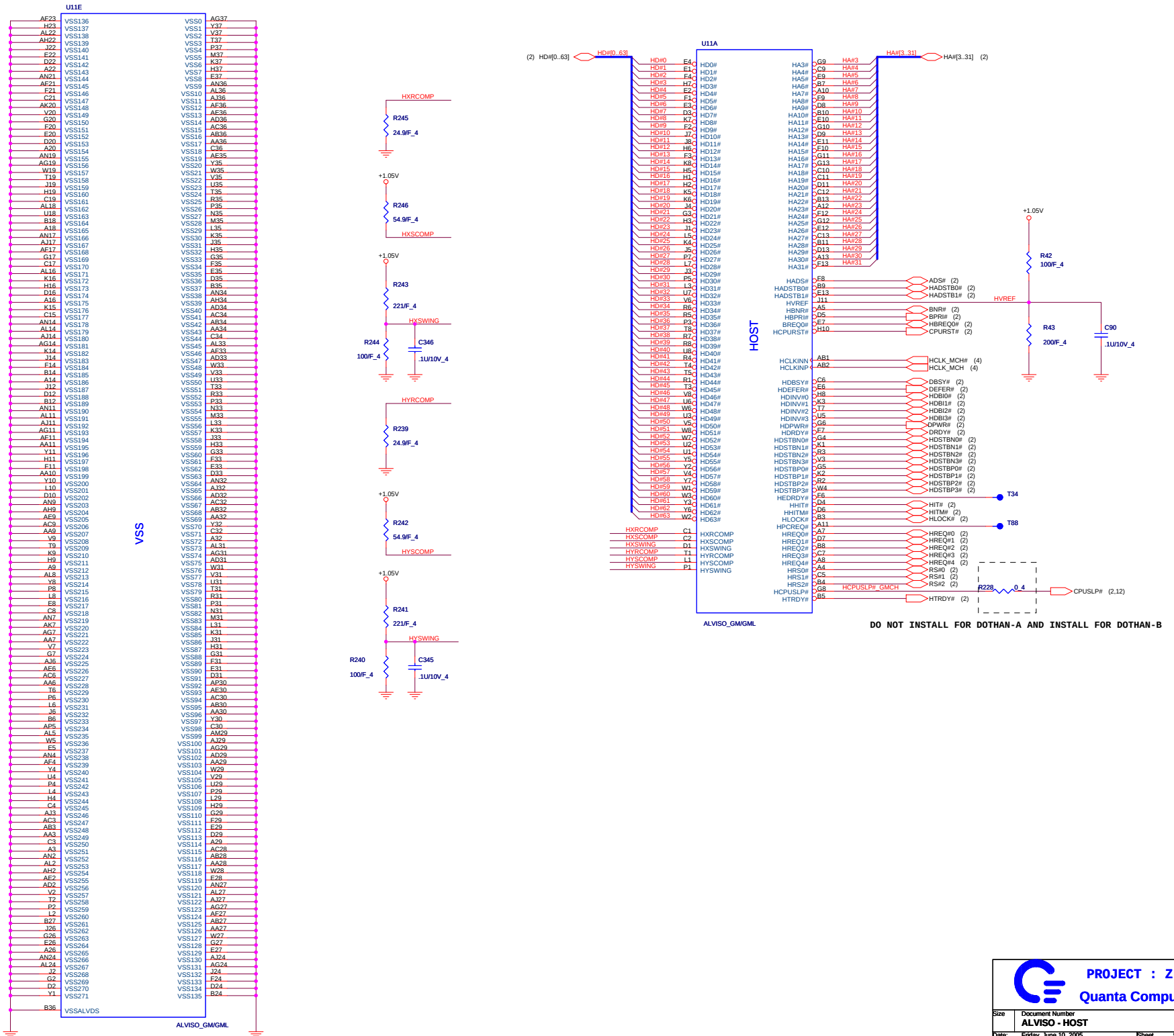
BOTHAN-A 533

SMbus address D2

PROJECT : ZL8

Quanta Computer Inc.

| Size | Document Number | Rev |
|---|---|---|
| | CLK GEN | 1A |
| Date: Friday, June 10, 2005 | Sheet 4 of 26 | |


U11E
VSS
ALVISO_GM/GML
U11A
HOST
ALVISO_GM/GML
HXRCOMP
HYSCOMP
HYSWING
R245
24.9/F_4
R246
54.9/F_4
R243
221/F_4
R244
100/F_4
C346
.1U/10V_4
R239
24.9/F_4
R242
54.9/F_4
R241
221/F_4
R240
100/F_4
C345
.1U/10V_4
+1.05V
HD#[0..63]
HA#[3..31]
R42
100/F_4
R43
200/F_4
C90
.1U/10V_4
HVREF
R228
0_4
T34
T88
CPUSLP#
HCPUSLP#_GMCH
DO NOT INSTALL FOR DOTHAN-A AND INSTALL FOR DOTHAN-B
PROJECT : ZL8
Quanta Computer Inc.
ALVISO - HOST
Friday, June 10, 2005
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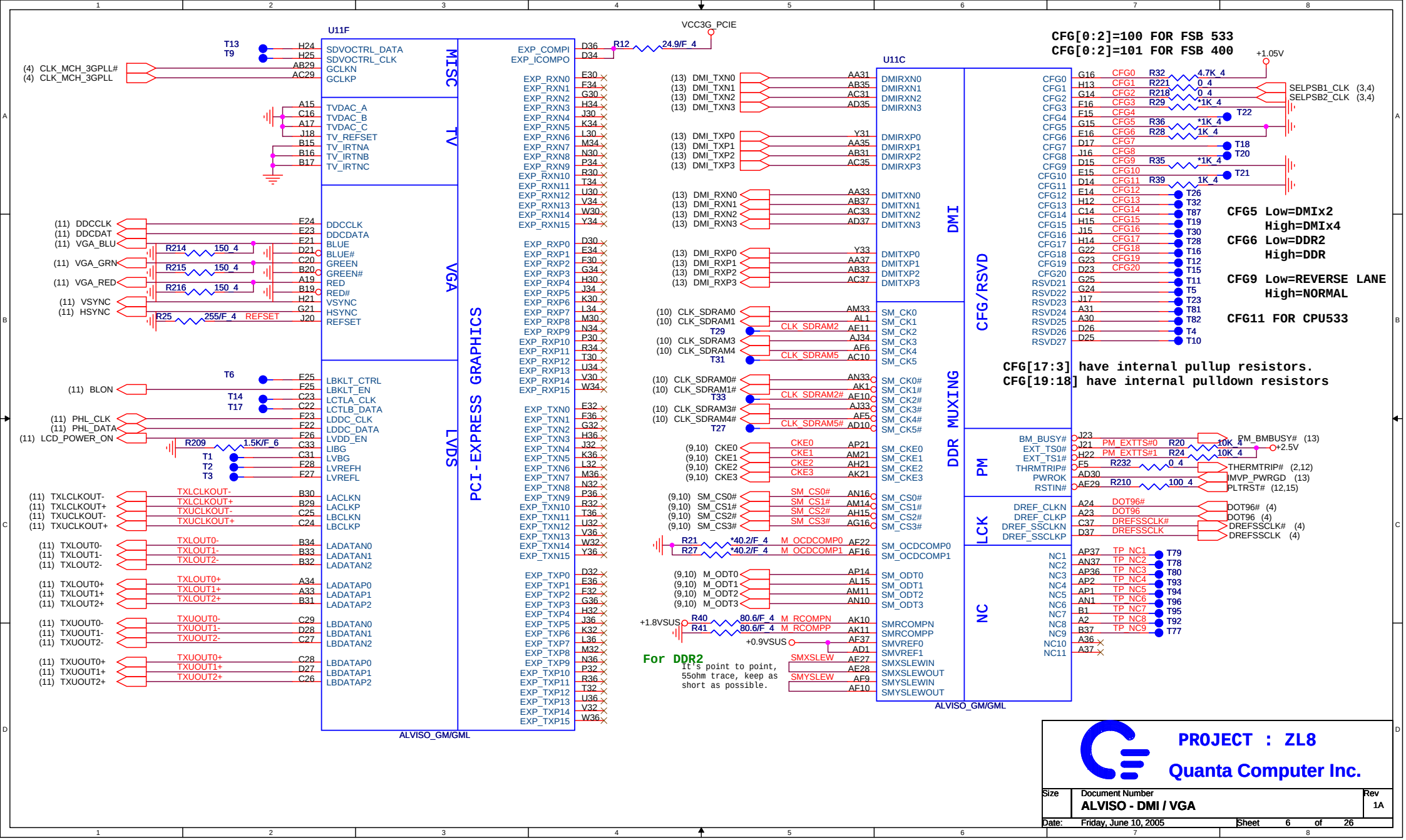
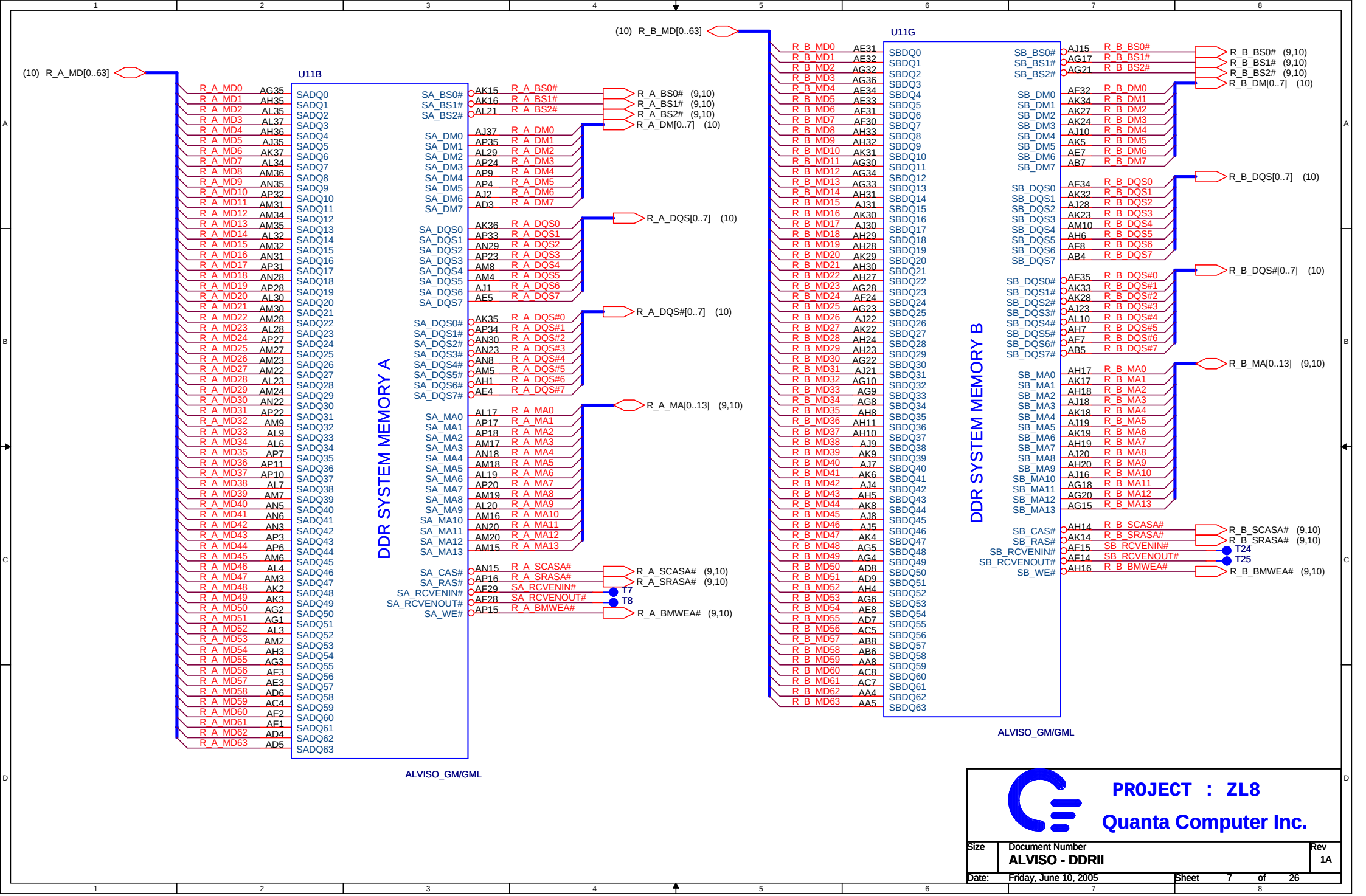

U11F
MISC
TV
VGA
LVDS
PCI-EXPRESS GRAPHICS
ALVISO_GM/GML
U11C
DMI
DDR MUXING
CFG/RSVD
PM
LCK
NC
ALVISO_GM/GML
VCC3G_PCIE
+1.05V
+1.8VSUS
+0.9VSUS
+2.5V
CFG[0:2]=100 FOR FSB 533
CFG[0:2]=101 FOR FSB 400
CFG5 Low=DMIx2
High=DMIx4
CFG6 Low=DDR2
High=DDR
CFG9 Low=REVERSE LANE
High=NORMAL
CFG11 FOR CPU533
CFG[17:3] have internal pullup resistors.
CFG[19:18] have internal pulldown resistors
For DDR2
It's point to point, 55ohm trace, keep as short as possible.
PROJECT : ZL8
Quanta Computer Inc.
Size
Document Number
ALVISO - DMI / VGA
Rev
1A
Date: Friday, June 10, 2005
Sheet 6 of 26


(10) R_A_MD[0..63]
U11B
DDR SYSTEM MEMORY A
ALVISO_GM/GML
(10) R_B_MD[0..63]
U11G
DDR SYSTEM MEMORY B
ALVISO_GM/GML
R_A_BS0# (9,10)
R_A_BS1# (9,10)
R_A_BS2# (9,10)
R_A_DM[0..7] (10)
R_A_DQS[0..7] (10)
R_A_DQS#[0..7] (10)
R_A_MA[0..13] (9,10)
R_A_SCASA# (9,10)
R_A_SRASA# (9,10)
R_A_BMWEA# (9,10)
R_B_BS0# (9,10)
R_B_BS1# (9,10)
R_B_BS2# (9,10)
R_B_DM[0..7] (10)
R_B_DQS[0..7] (10)
R_B_DQS#[0..7] (10)
R_B_MA[0..13] (9,10)
R_B_SCASA# (9,10)
R_B_SRASA# (9,10)
R_B_BMWEA# (9,10)
T7
T8
T24
T25
PROJECT : ZL8
Quanta Computer Inc.
Size
Document Number
ALVISO - DDRII
Rev
1A
Date: Friday, June 10, 2005
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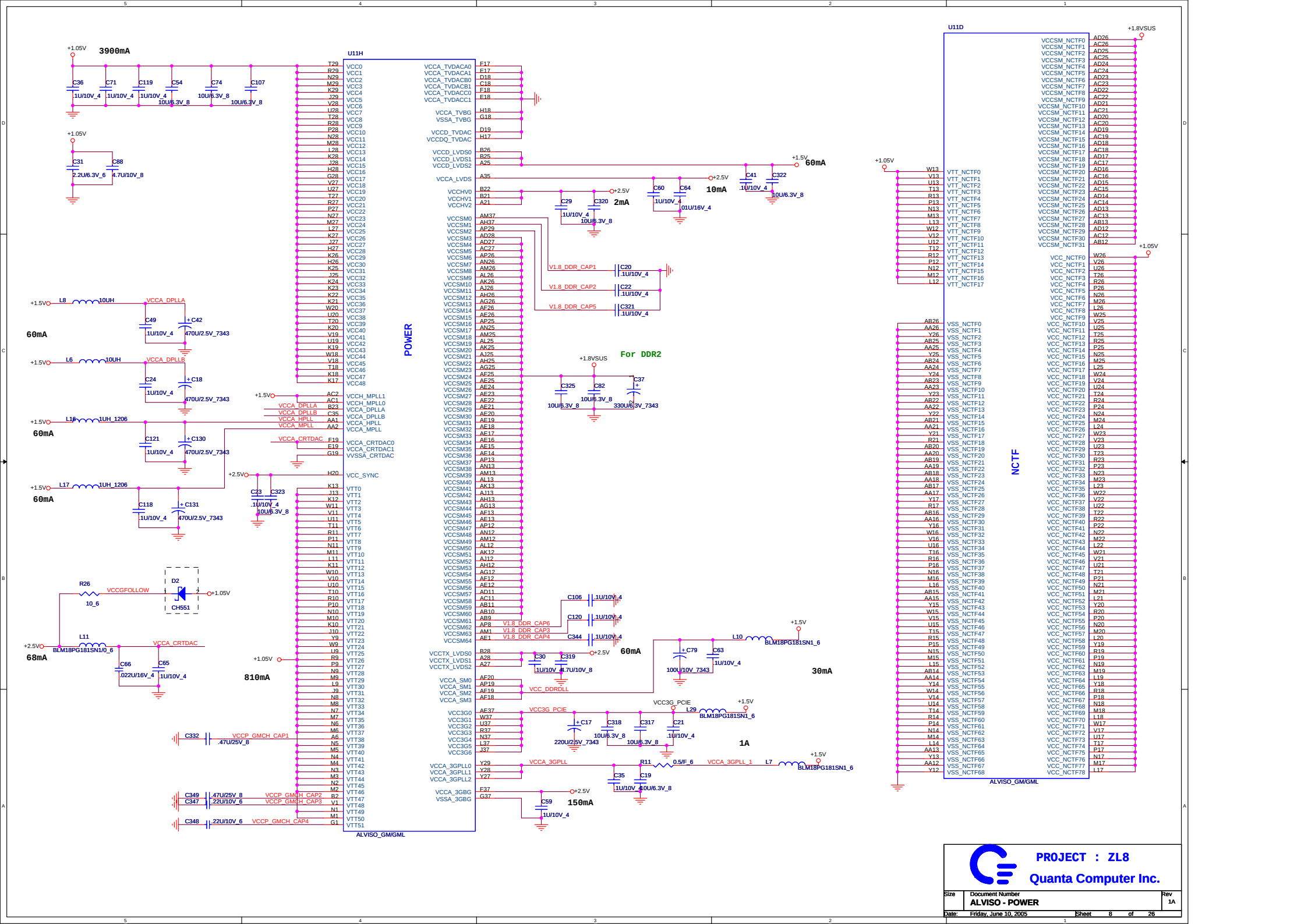
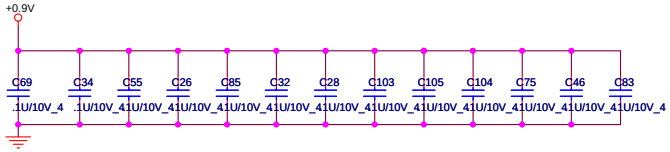
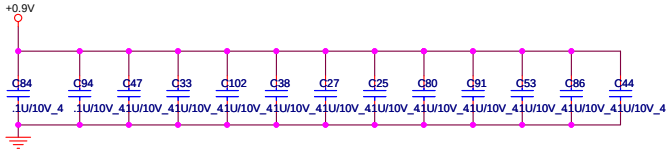
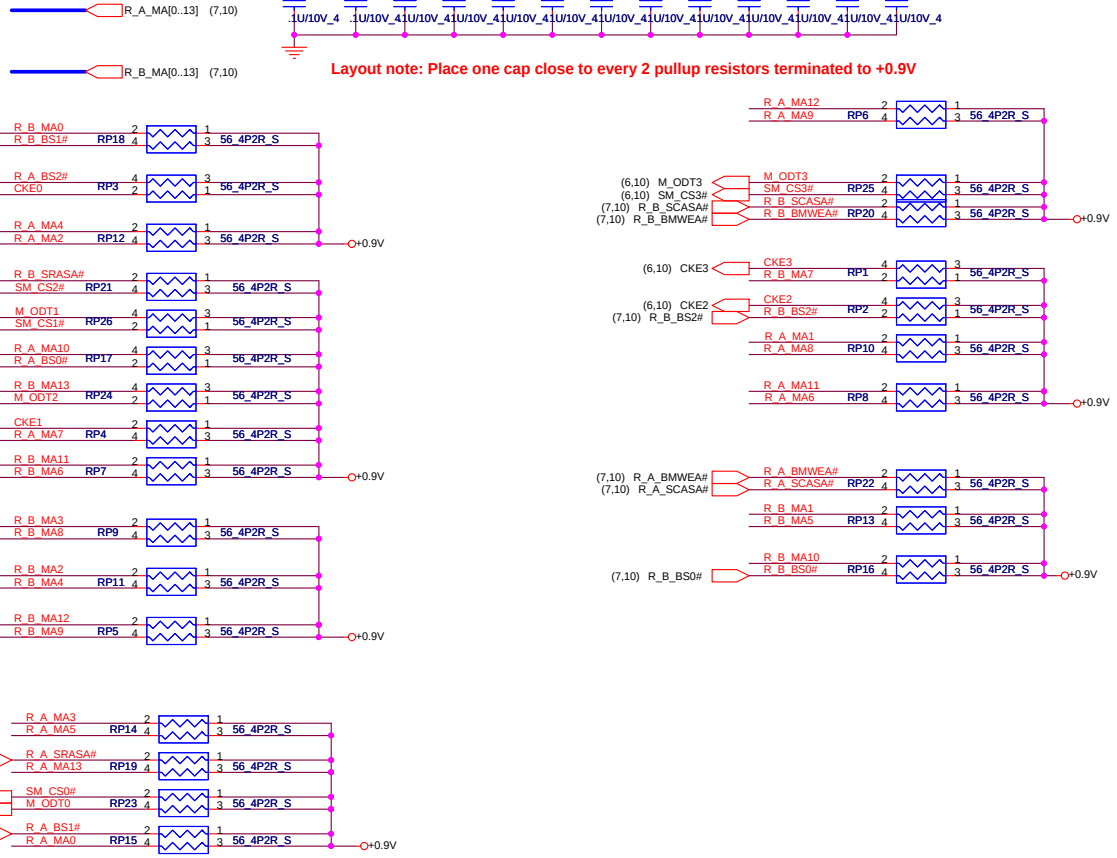

U11H
POWER
ALVISO_GM/GML
U11D
NCTF
ALVISO_GM/GML
3900mA
60mA
10mA
2mA
For DDR2
810mA
68mA
150mA
30mA
1A
PROJECT : ZL8
Quanta Computer Inc.
ALVISO - POWER
Friday, June 10, 2005




Layout note: Place one cap close to every 2 pullup resistors terminated to +0.9V




Layout note: Place one cap close to every 2 pullup resistors terminated to +0.9V




PROJECT : ZL8

Quanta Computer Inc.

| Size | Document Number | Rev |
|---|---|---|
| | DDR2 TERMINATION | 1A |

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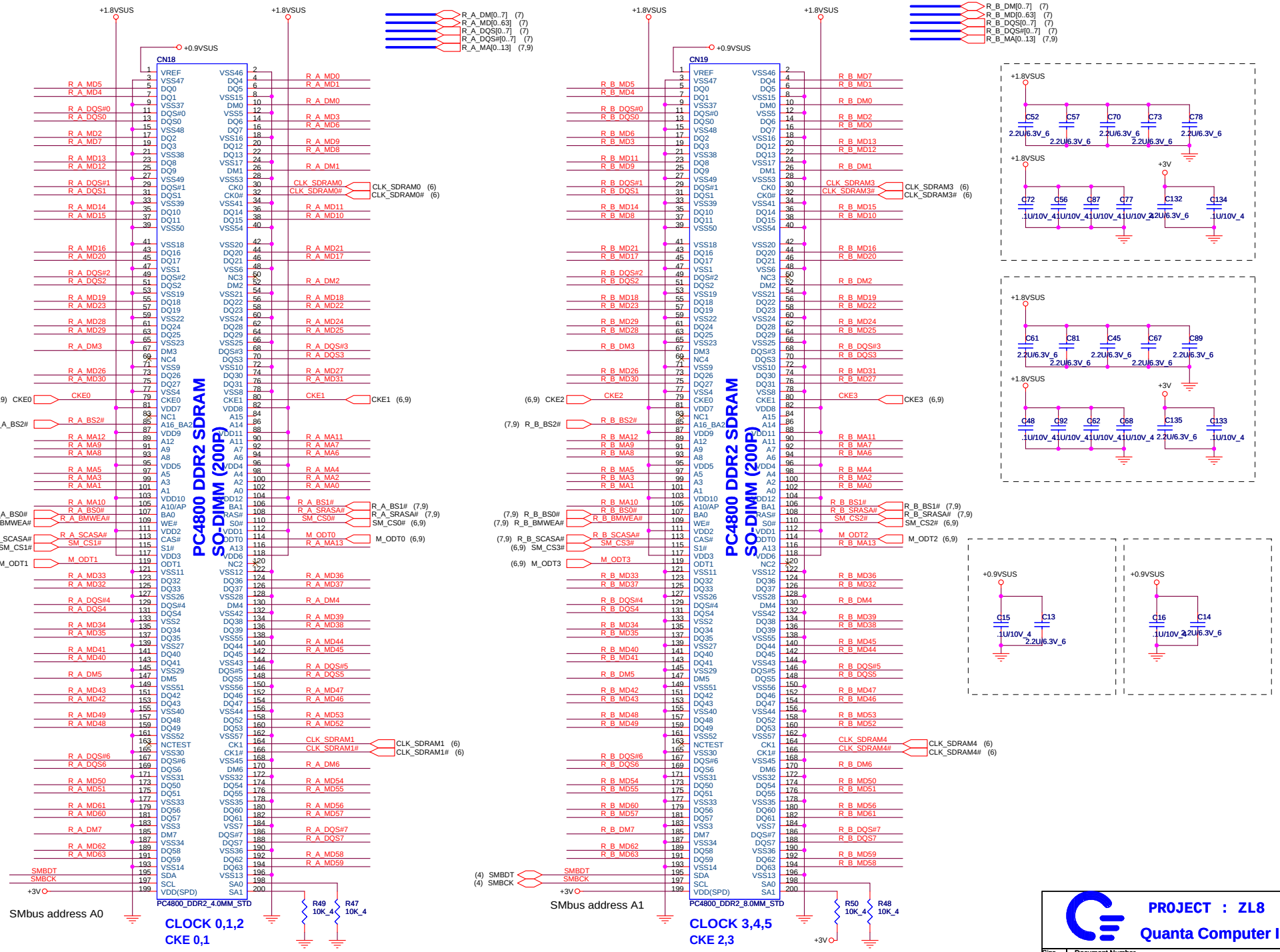


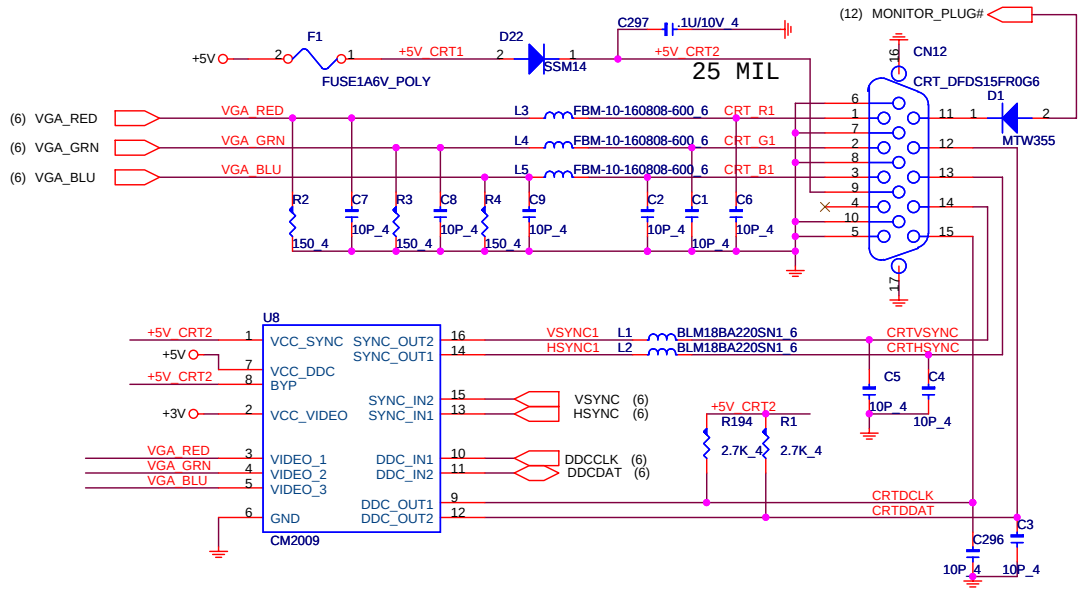

PROJECT : ZL8

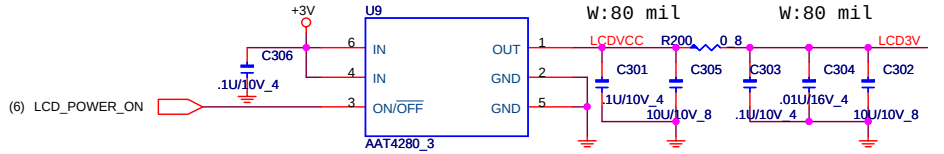
Quanta Computer Inc.

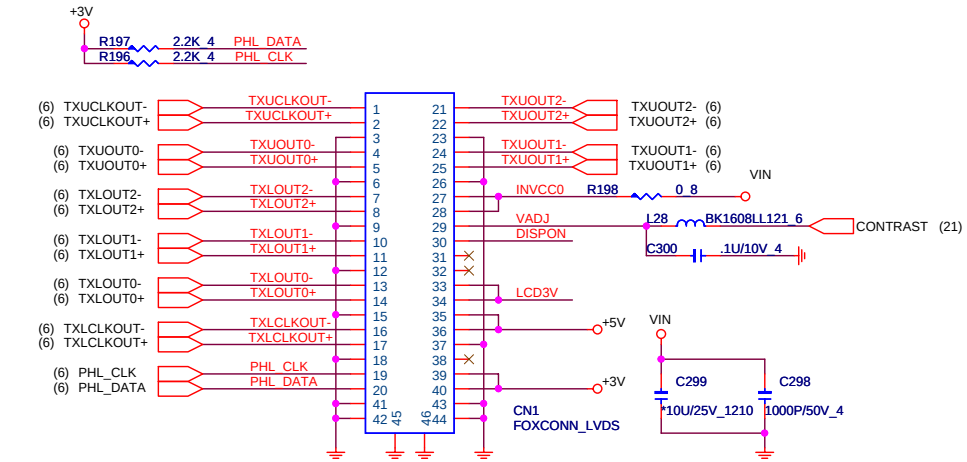
| Size | Document Number | Rev |
|---|---|---|
| | DDR2 SO-DIMM ( 200P ) | 1A |

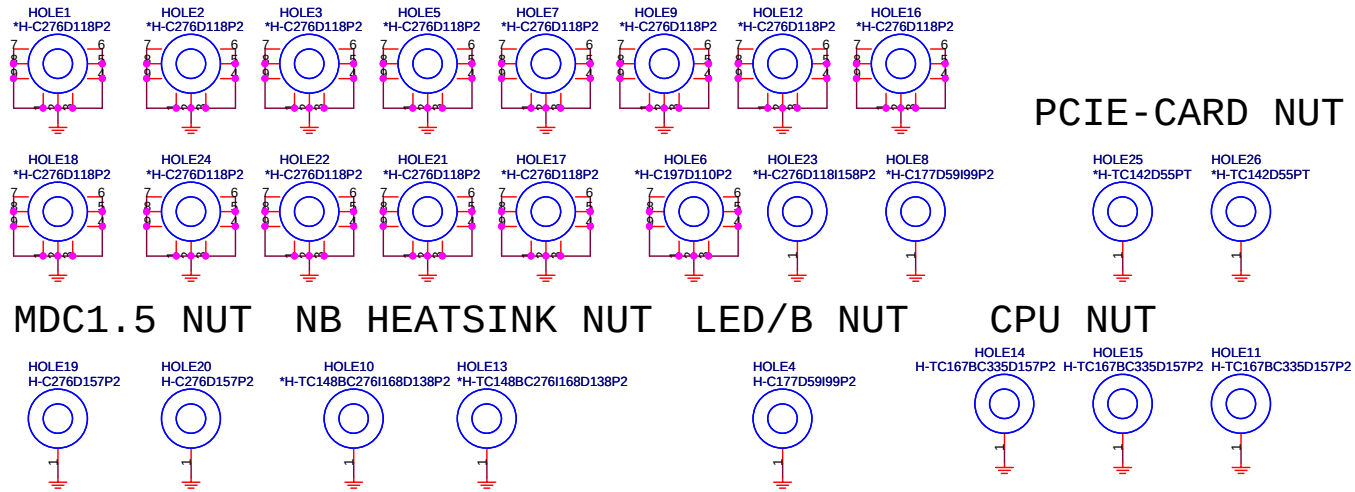
Date: Friday, June 10, 2005 Sheet 10 of 26

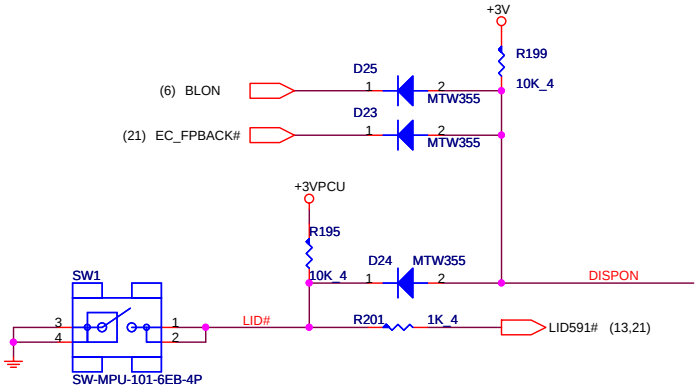
# CRT PORT



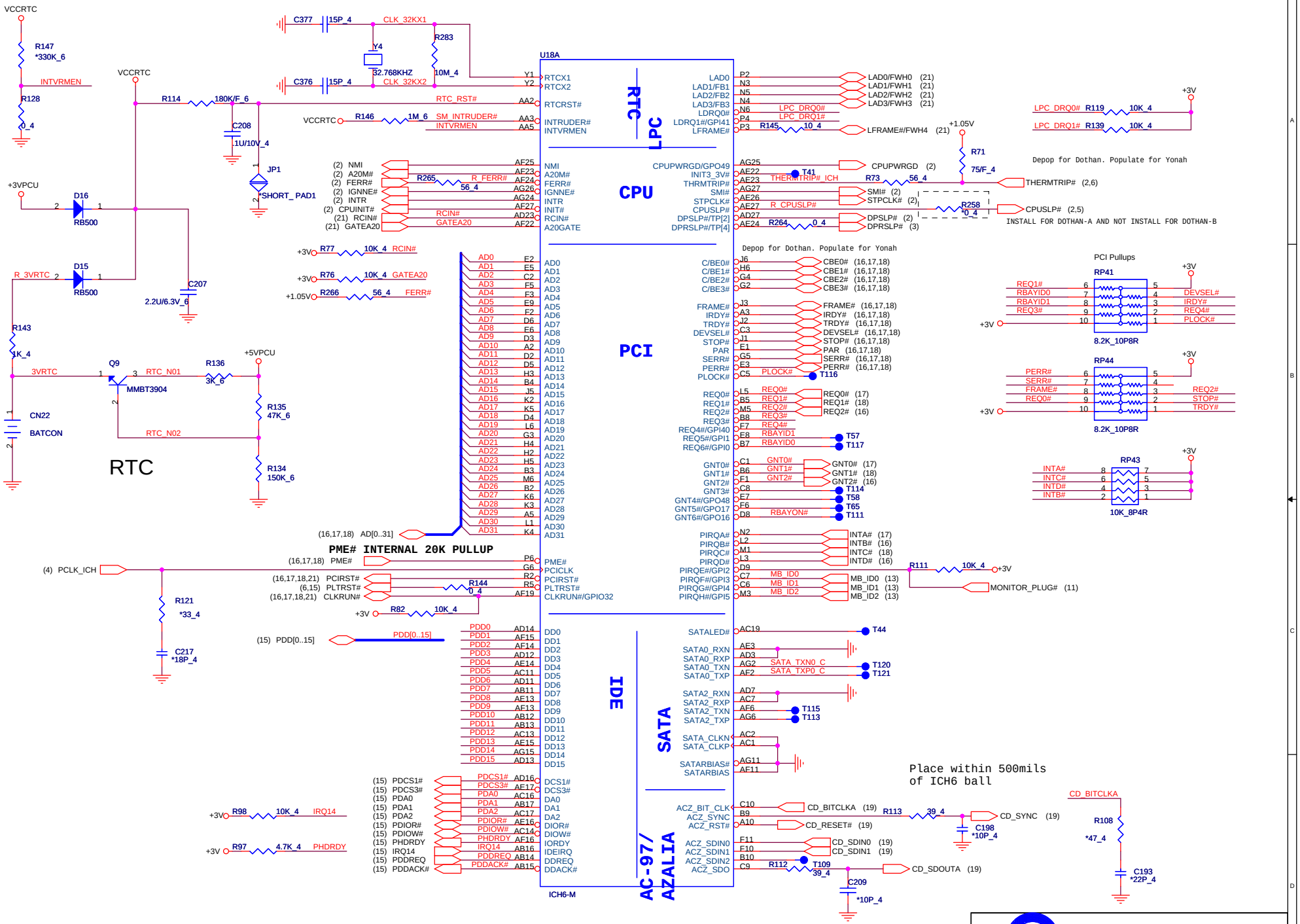



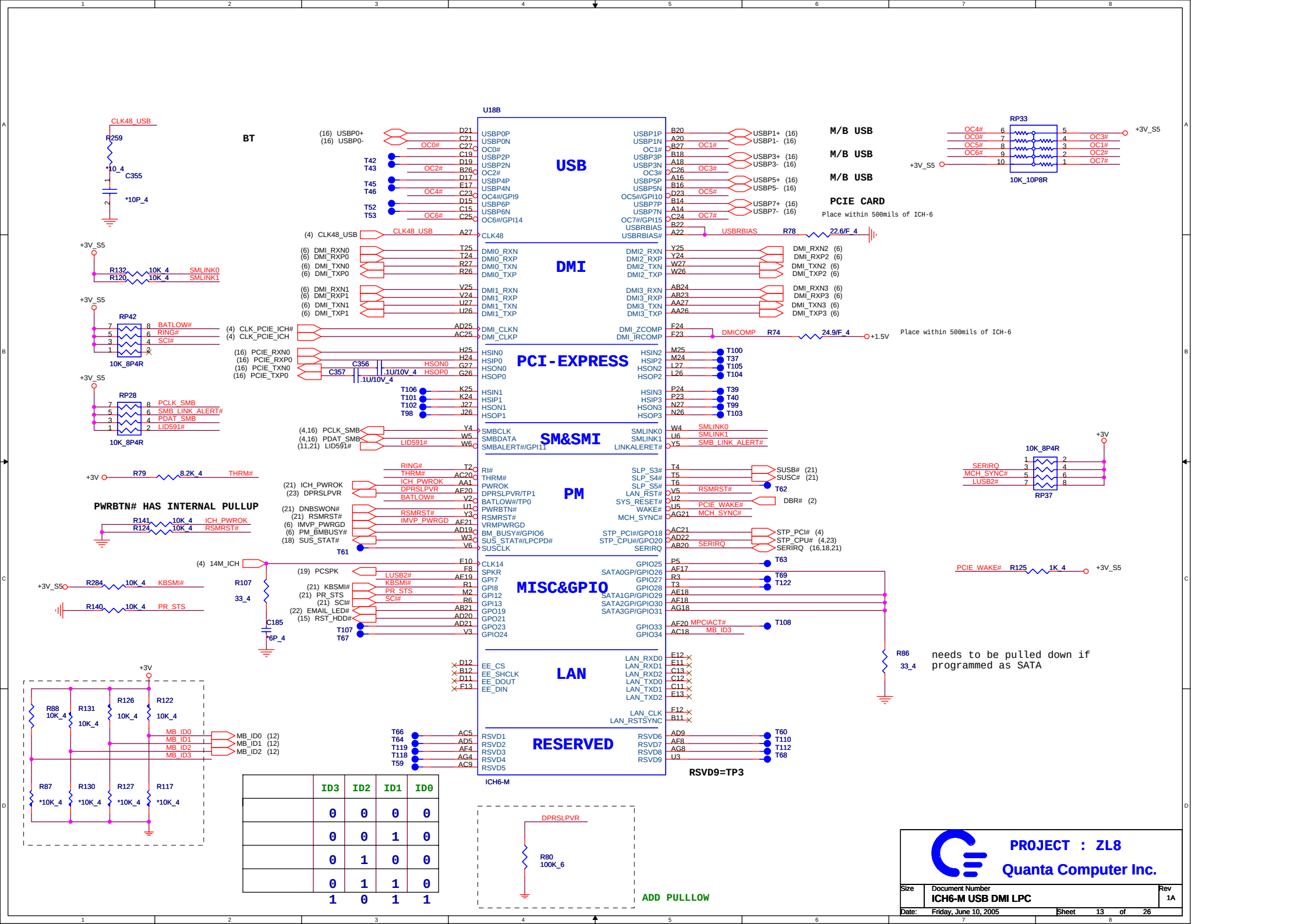

## Lid Switch

PROJECT : ZL8

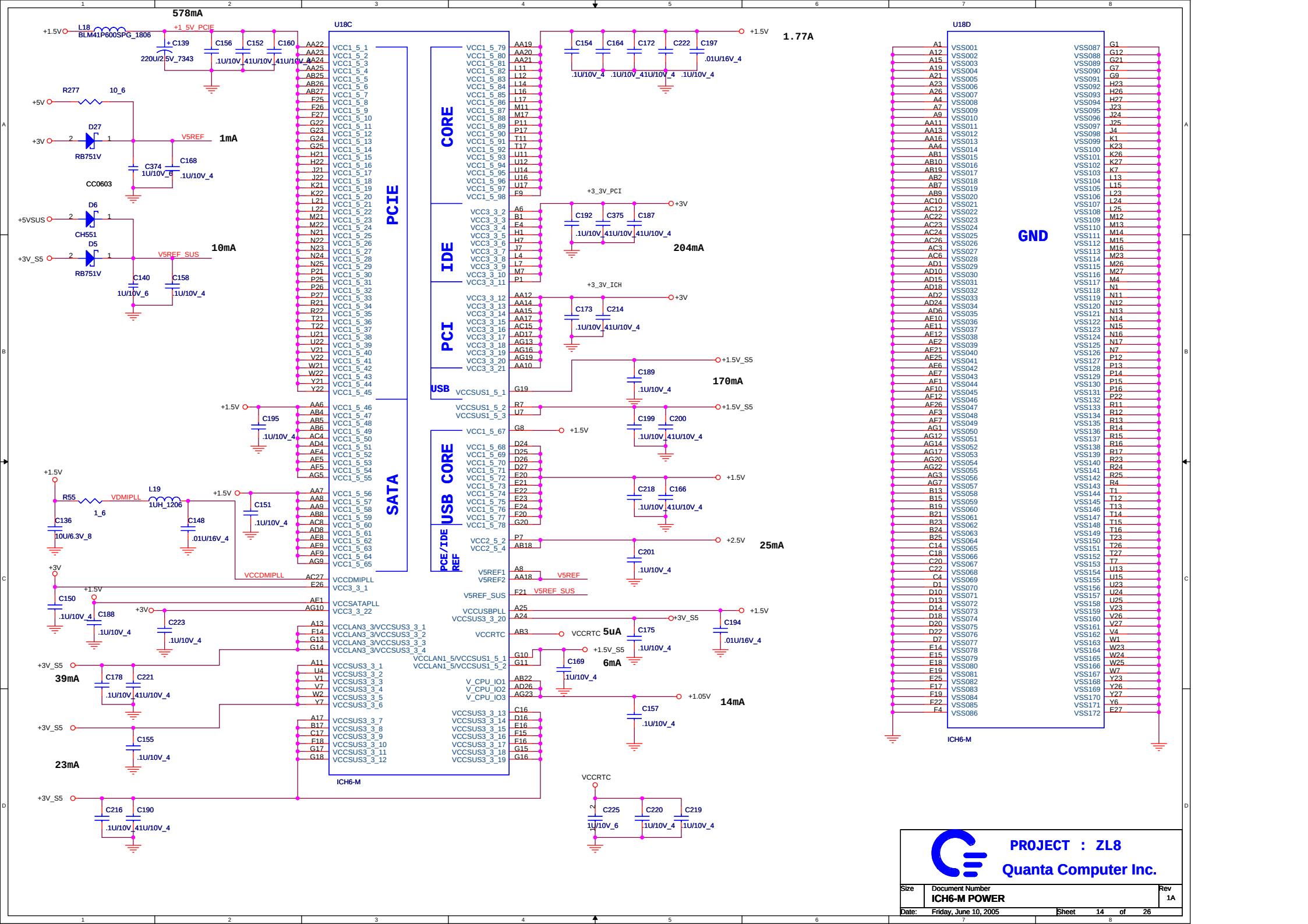
Quanta Computer Inc.

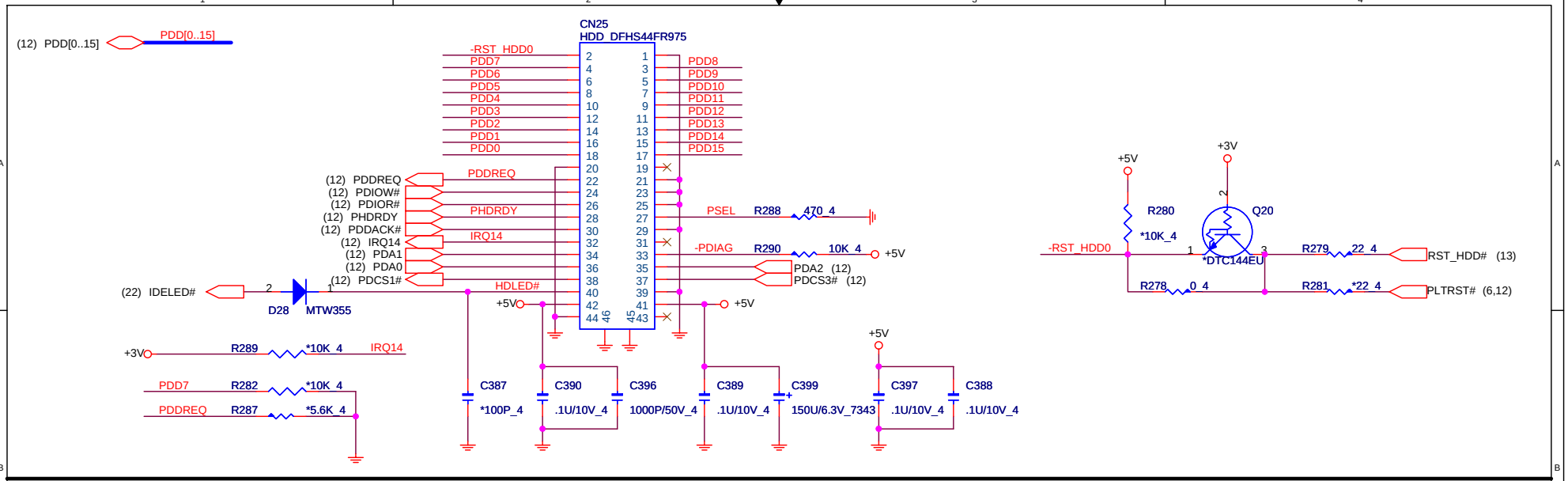
| Size | Document Number | Rev |
|---|---|---|
| | LVDS,VGA Ports, LID, & HOLES | 1A |
| Date: Friday, June 10, 2005 | Sheet 11 of 26 | |


RTC
CPU
PCI
IDE
SATA
LPC
AC-97/ AZALIA
ICH6-M
U18A
PME# INTERNAL 20K PULLUP
Place within 500mils of ICH6 ball
Depop for Dothan. Populate for Yonah
INSTALL FOR DOTHAN-A AND NOT INSTALL FOR DOTHAN-B
PCI Pullups
PROJECT : ZL8
Quanta Computer Inc.
ICH6-M CPU PCI IDE AC97
Friday, June 10, 2005
Sheet 12 of 26

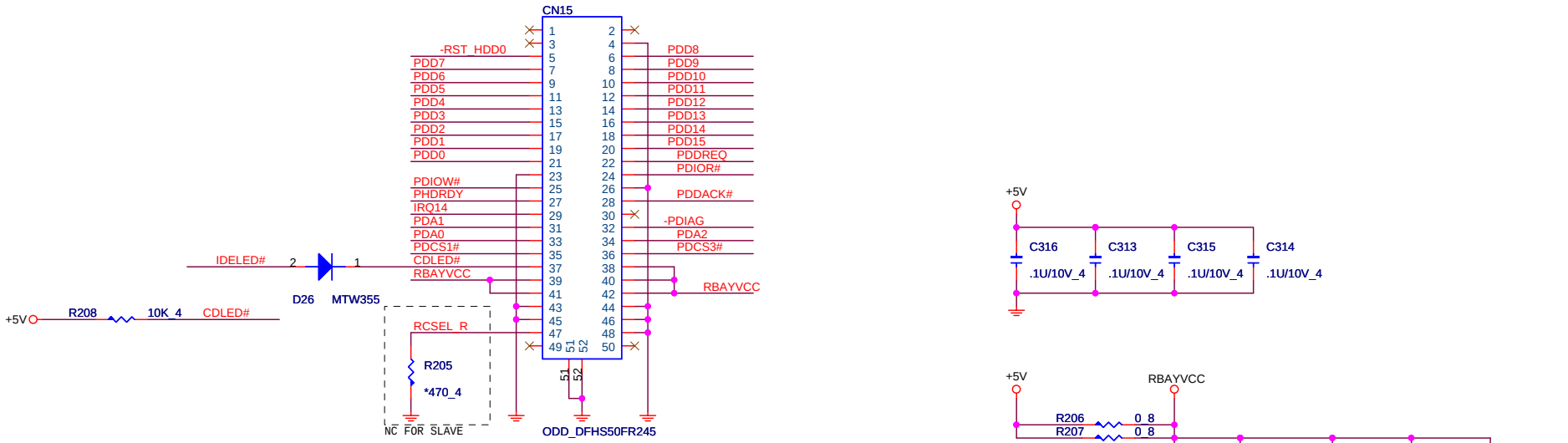


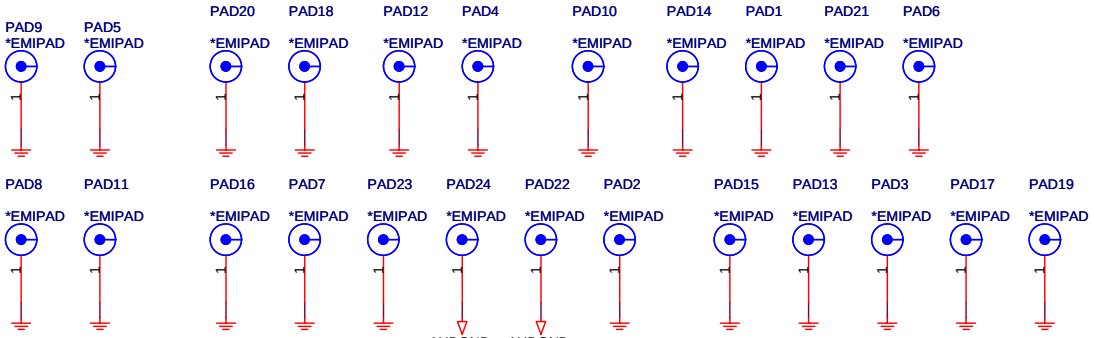

| | ID3 | ID2 | ID1 | ID0 |
|---|---|---|---|---|
| | 0 | 0 | 0 | 0 |
| | 0 | 0 | 1 | 0 |
| | 0 | 1 | 0 | 0 |
| | 0 | 1 | 1 | 0 |
| | 1 | 0 | 1 | 1 |


578mA
1.77A
U18C
U18D
CORE
PCIE
IDE
PCI
USB
USB CORE
SATA
PCE/IDE REF
GND
ICH6-M
1mA
10mA
204mA
170mA
25mA
5uA
6mA
14mA
39mA
23mA
PROJECT : ZL8
Quanta Computer Inc.
ICH6-M POWER
Friday, June 10, 2005
Sheet 14 of 26
Rev 1A




## ODD Connector




**PROJECT : ZL8**

**Quanta Computer Inc.**

| Size | Document Number | Rev |
|---|---|---|
| | HDD & CDROM | 1A |
| Date: Friday, June 10, 2005 | Sheet 15 of 26 | |

## MINI-PCI

ID Select : AD20
Interrupt Pin : INTB# , INTD#
Request Indicate : REQ2#
Grant Indicate : GNT2#

CN26 — MINIPCI_DGMC4000382

CN23 — *67910-0002

Q8 *DTC144EU

U15 U16 U7 — TPS2061DGNR

L31 L30 L33 L32 L24 L36 — DLW21HN900SQ2L

L12 — DLW21HN900SQ2L

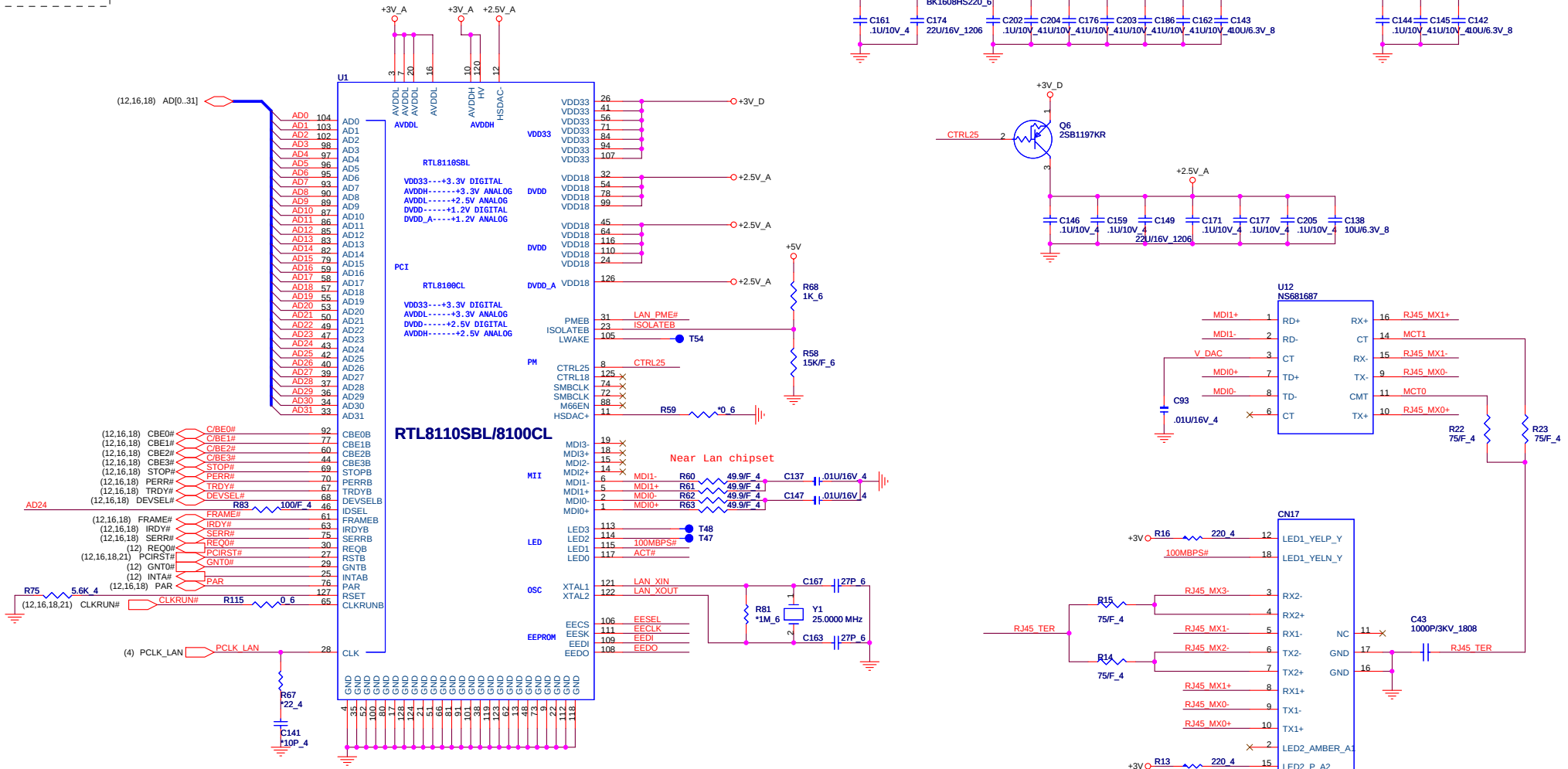
Q5 AO3403

CN20 CN21 CN27 — USB_DFHD04MR671

CN4 — PTWO_MINIUSB

| | | |
|---|---|---|
| PROJECT : ZL8 | | |
| Quanta Computer Inc. | | |
| Size | Document Number: MINI PCI,USB | Rev: 1A |
| Date: Friday, June 10, 2005 | Sheet 16 of 26 | |

ID Select : AD24
Interrupt Pin : INTA#
Request Indicate : REQ0#
Grant Indicate : GNT0#




PROJECT : ZL8

Quanta Computer Inc.

| Size | Document Number | Rev |
|---|---|---|
| | LAN RTL8110SBL/8100CL | 1A |

Date: Monday, June 13, 2005 Sheet 17 of 26

ID Select : AD17
Interrupt Pin : INTC#
Request Indicate : REQ1#
Grant Indicate : GNT1#

# CARDBUS SLOT

CN24

U4-1 PCI1510PGE

U4-2 PCI1510PGE

U4-4 PCI1510PGE

U6 TPS2211A

FOX_WZ21131-G2

Near Slot

Near Chipset

+3V Plane

Near Chipset

PROJECT : ZL8

Quanta Computer Inc.

| Size | Document Number | Rev |
|---|---|---|
| | PCMCIA CONTROLLER | 1A |

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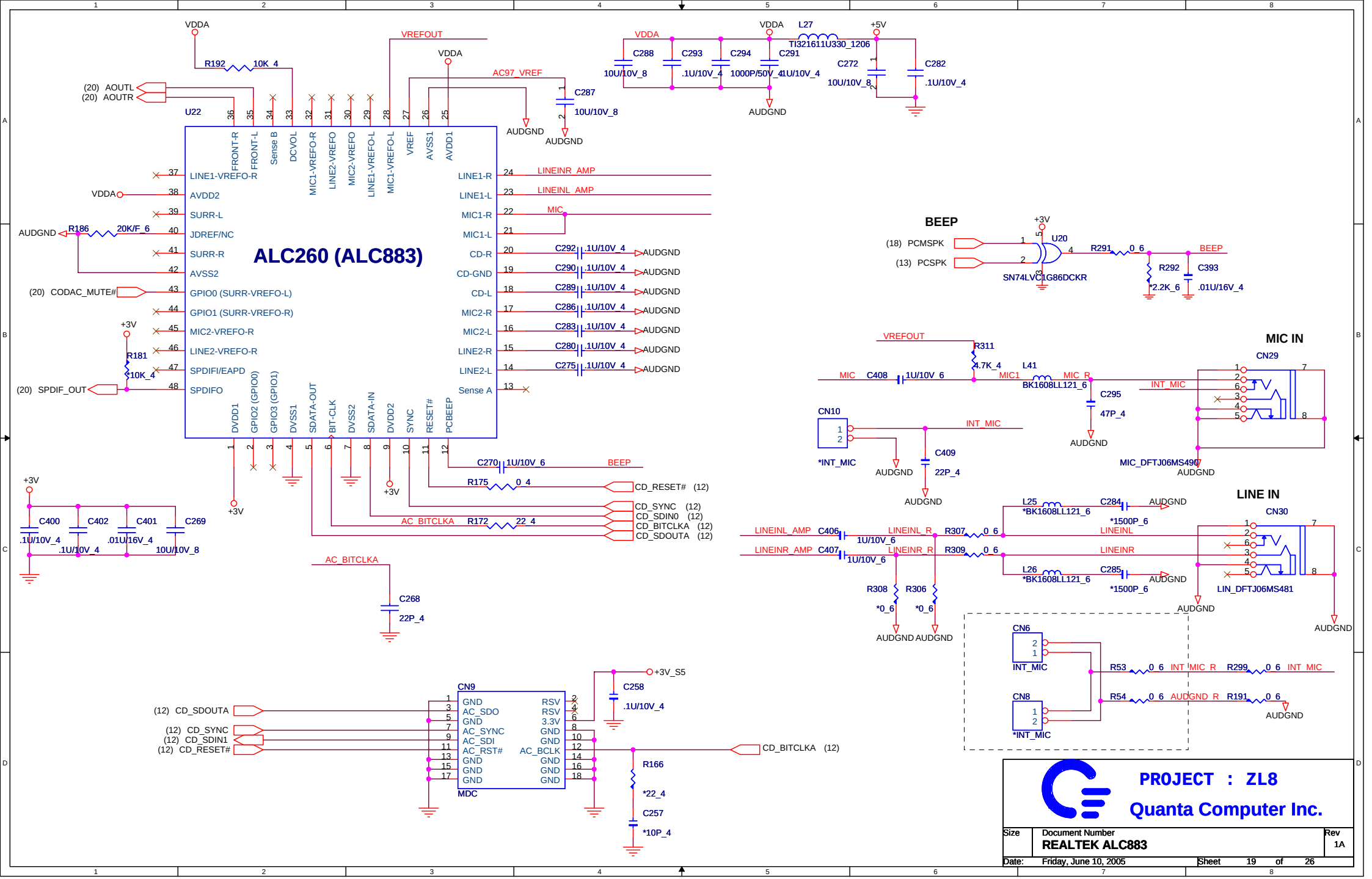
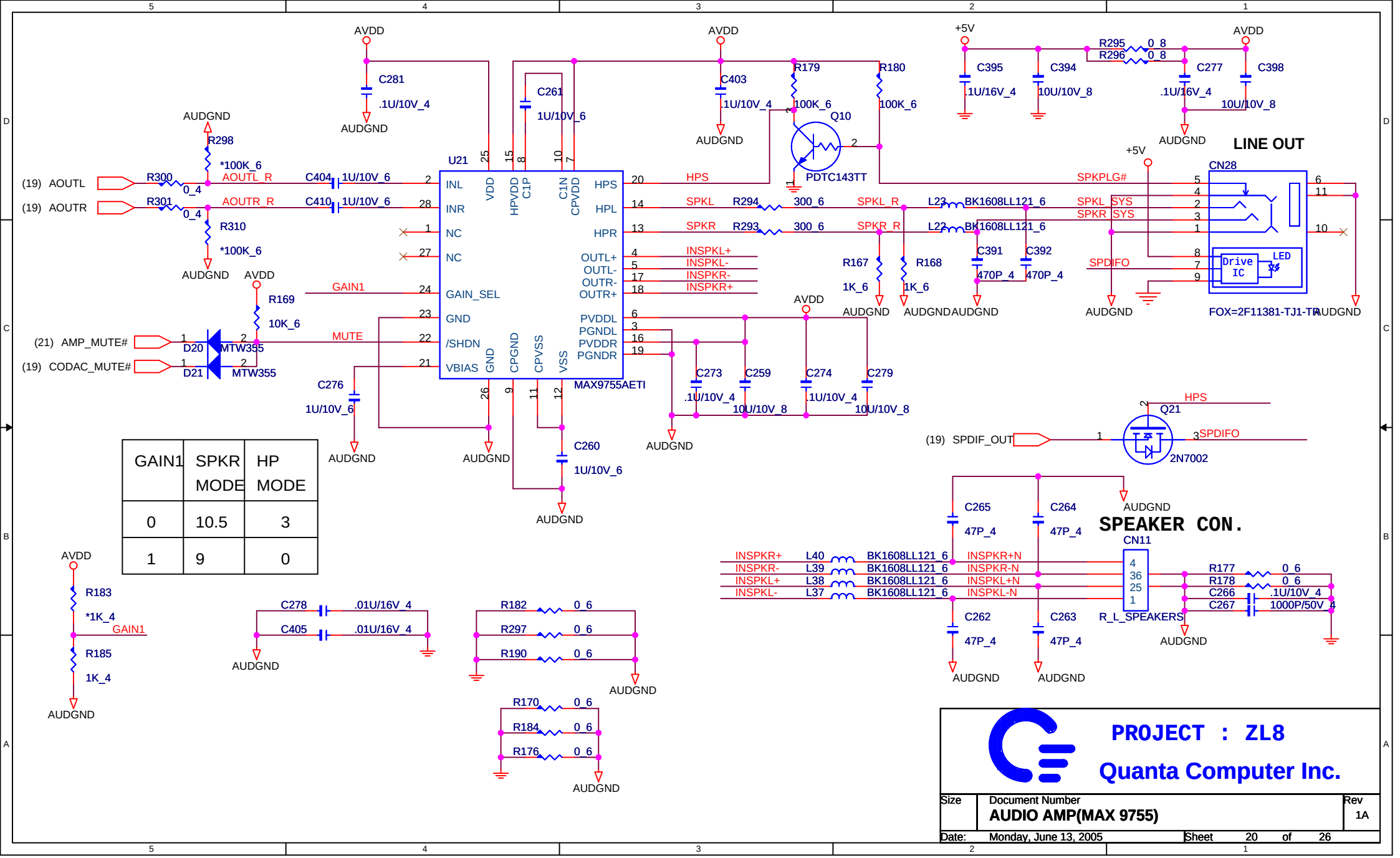
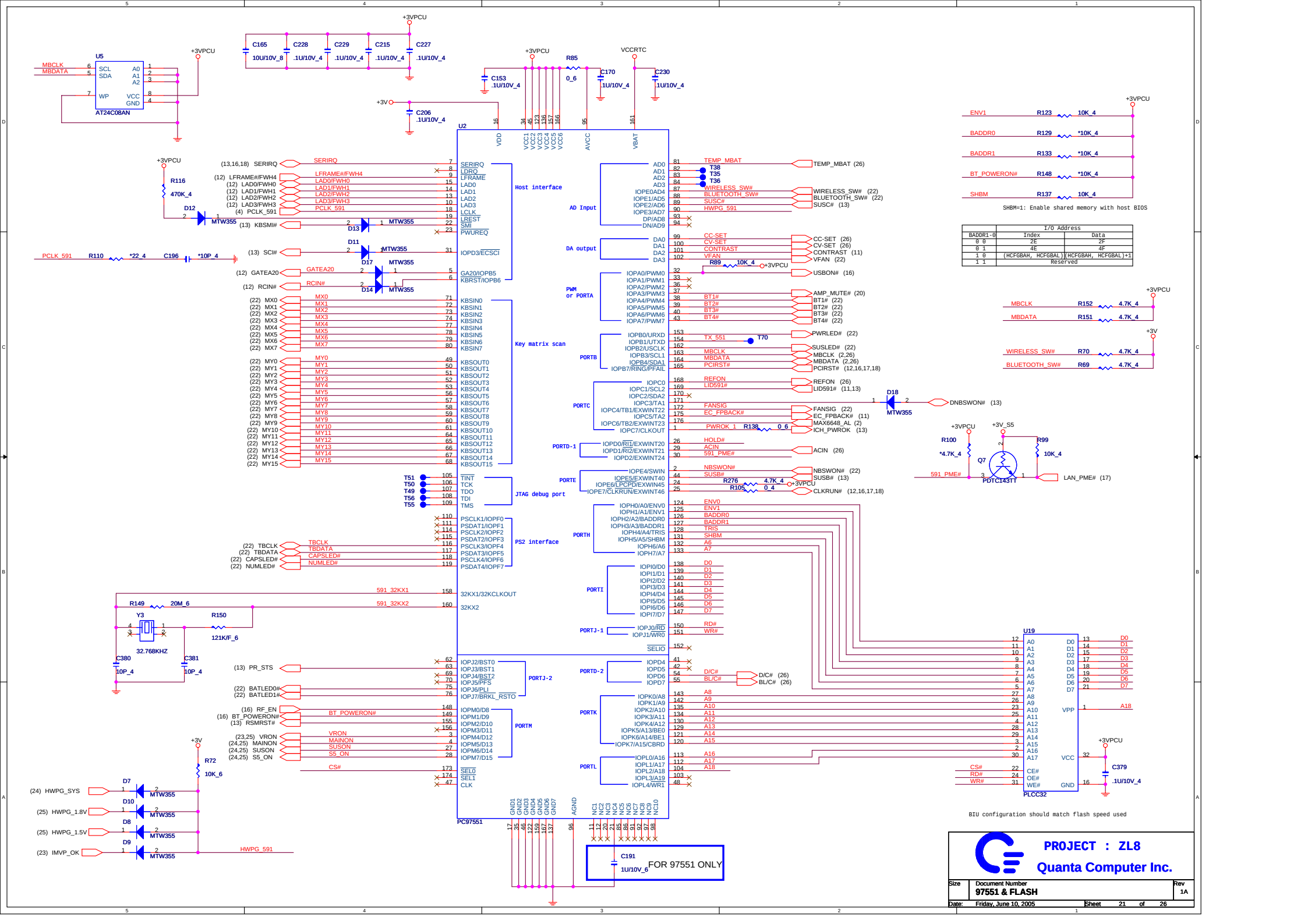

ALC260 (ALC883)
U22
VDDA
VREFOUT
AC97_VREF
AUDGND
R192 10K_4
(20) AOUTL
(20) AOUTR
C287 10U/10V_8
C288 10U/10V_8
C293 .1U/10V_4
C294 1000P/50V_4
C291 1U/10V_4
L27 TI321611U330_1206
C272 10U/10V_8
C282 .1U/10V_4
FRONT-R
FRONT-L
Sense B
DCVOL
MIC1-VREFO-R
LINE2-VREFO
MIC2-VREFO
LINE1-VREFO-L
MIC1-VREFO-L
VREF
AVSS1
AVDD1
LINE1-VREFO-R
AVDD2
SURR-L
JDREF/NC
SURR-R
AVSS2
GPIO0 (SURR-VREFO-L)
GPIO1 (SURR-VREFO-R)
MIC2-VREFO-R
LINE2-VREFO-R
SPDIFI/EAPD
SPDIFO
LINE1-R
LINE1-L
MIC1-R
MIC1-L
CD-R
CD-GND
CD-L
MIC2-R
MIC2-L
LINE2-R
LINE2-L
Sense A
LINEINR_AMP
LINEINL_AMP
MIC
R186 20K/F_6
(20) CODAC_MUTE#
R181 10K_4
(20) SPDIF_OUT
C292 .1U/10V_4
C290 .1U/10V_4
C289 .1U/10V_4
C286 .1U/10V_4
C283 .1U/10V_4
C280 .1U/10V_4
C275 .1U/10V_4
DVDD1
GPIO2 (GPIO0)
GPIO3 (GPIO1)
DVSS1
SDATA-OUT
BIT-CLK
DVSS2
SDATA-IN
DVDD2
SYNC
RESET#
PCBEEP
C270 1U/10V_6
BEEP
R175 0_4
CD_RESET# (12)
CD_SYNC (12)
CD_SDIN0 (12)
CD_BITCLKA (12)
CD_SDOUTA (12)
AC_BITCLKA
R172 22_4
C400 .1U/10V_4
C402 .1U/10V_4
C401 .01U/16V_4
C269 10U/10V_8
C268 22P_4
BEEP
(18) PCMSPK
(13) PCSPK
U20 SN74LVC1G86DCKR
R291 0_6
R292 *2.2K_6
C393 .01U/16V_4
MIC IN
CN29
VREFOUT
R311 4.7K_4
MIC C408 1U/10V_6
MIC1
L41 BK1608LL121_6
MIC_R
INT_MIC
C295 47P_4
CN10
*INT_MIC
C409 22P_4
MIC_DFTJ06MS490
LINE IN
CN30
LINEINL_AMP C406 1U/10V_6
LINEINR_AMP C407 1U/10V_6
LINEINL_R
LINEINR_R
R307 0_6
R309 0_6
L25 *BK1608LL121_6
L26 *BK1608LL121_6
C284 *1500P_6
C285 *1500P_6
LINEINL
LINEINR
R308 *0_6
R306 *0_6
LIN_DFTJ06MS481
CN6
CN8
INT_MIC
*INT_MIC
R53 0_6
R54 0_6
R299 0_6
R191 0_6
AUDGND_R
CN9
MDC
(12) CD_SDOUTA
(12) CD_SYNC
(12) CD_SDIN1
(12) CD_RESET#
GND
AC_SDO
AC_SYNC
AC_SDI
AC_RST#
RSV
3.3V
AC_BCLK
+3V_S5
C258 .1U/10V_4
R166 *22_4
C257 *10P_4
CD_BITCLKA (12)
PROJECT : ZL8
Quanta Computer Inc.
Size
Document Number
REALTEK ALC883
Rev
1A
Date: Friday, June 10, 2005
Sheet 19 of 26




| GAIN1 | SPKR MODE | HP MODE |
|---|---|---|
| 0 | 10.5 | 3 |
| 1 | 9 | 0 |


U5
AT24C08AN
MBCLK
MBDATA
SCL
SDA
WP
A0
A1
A2
VCC
GND
+3VPCU
C165 10U/10V_8
C228 .1U/10V_4
C229 .1U/10V_4
C215 .1U/10V_4
C227 .1U/10V_4
C153 .1U/10V_4
R85 0_6
C170 .1U/10V_4
C230 .1U/10V_4
VCCRTC
+3V
C206 .1U/10V_4
U2
PC97551
VDD
VCC1
VCC2
VCC3
VCC4
VCC5
VCC6
AVCC
VBAT
Host interface
SERIRQ
LDRQ
LFRAME
LAD0
LAD1
LAD2
LAD3
LCLK
LRESET
SMI
PWUREQ
AD Input
DA output
PWM or PORTA
PORTB
Key matrix scan
PORTC
PORTD-1
PORTE
JTAG debug port
PS2 interface
PORTH
PORTI
PORTJ-1
PORTJ-2
PORTD-2
PORTK
PORTM
PORTL
SELIO
32KX1/32KCLKOUT
32KX2
TEMP_MBAT (26)
WIRELESS_SW# (22)
BLUETOOTH_SW# (22)
SUSC# (13)
HWPG_591
CC-SET (26)
CV-SET (26)
CONTRAST (11)
VFAN (22)
USBON# (16)
AMP_MUTE# (20)
BT1# (22)
BT2# (22)
BT3# (22)
BT4# (22)
PWRLED# (22)
SUSLED# (22)
MBCLK (2,26)
MBDATA (2,26)
PCIRST# (12,16,17,18)
REFON (26)
LID591# (11,13)
DNBSWON# (13)
FANSIG (22)
EC_FPBACK# (11)
MAX6648_AL (2)
ICH_PWROK (13)
ACIN (26)
NBSWON# (22)
SUSB# (13)
CLKRUN# (12,16,17,18)
D/C# (26)
BL/C# (26)
CS-SET
CV-SET
CONTRAST
VFAN
BT1#
BT2#
BT3#
BT4#
TX_551
MBCLK
MBDATA
PCIRST#
REFON
LID591#
FANSIG
EC_FPBACK#
PWROK_1
HOLD#
ACIN
591_PME#
SUSB#
CLKRUN#
D/C#
BL/C#
SERIRQ
LFRAME#/FWH4
LAD0/FWH0
LAD1/FWH1
LAD2/FWH2
LAD3/FWH3
PCLK_591
KBSMI#
SCI#
GATEA20
RCIN#
MX0
MX1
MX2
MX3
MX4
MX5
MX6
MX7
MY0
MY1
MY2
MY3
MY4
MY5
MY6
MY7
MY8
MY9
MY10
MY11
MY12
MY13
MY14
MY15
TINT
TCK
TDO
TDI
TMS
TBCLK
TBDATA
CAPSLED#
NUMLED#
591_32KX1
591_32KX2
Y3
32.768KHZ
R149 20M_6
R150 121K/F_6
C380 10P_4
C381 10P_4
PR_STS
BATLED0#
BATLED1#
RF_EN
BT_POWERON#
RSMRST#
BT_POWERON#
VRON
MAINON
SUSON
S5_ON
CS#
R72 10K_6
HWPG_SYS
HWPG_1.8V
HWPG_1.5V
IMVP_OK
HWPG_591
D7
D10
D8
D9
MTW355
R116 470K_4
D12
D13
D11
D17
D14
PCLK_591
R110 *22_4
C196 *10P_4
R89 10K_4
R138 0_6
R276 4.7K_4
R105 0_4
T51
T50
T49
T56
T55
T35
T36
T38
T70
ENV1 R123 10K_4
BADDR0 R129 *10K_4
BADDR1 R133 *10K_4
BT_POWERON# R148 *10K_4
SHBM R137 10K_4
SHBM=1: Enable shared memory with host BIOS
I/O Address
BADDR1-0 | Index | Data
0 0 | 2E | 2F
0 1 | 4E | 4F
1 0 | (HCFGBAH, HCFGBAL) | (HCFGBAH, HCFGBAL)+1
1 1 | Reserved
MBCLK R152 4.7K_4
MBDATA R151 4.7K_4
WIRELESS_SW# R70 4.7K_4
BLUETOOTH_SW# R69 4.7K_4
R100 *4.7K_4
R99 10K_4
Q7 PDTC143TT
LAN_PME# (17)
U19
PLCC32
CS#
RD#
WR#
VPP
VCC
GND
C379 .1U/10V_4
BIU configuration should match flash speed used
C191 1U/10V_6 FOR 97551 ONLY
PROJECT : ZL8
Quanta Computer Inc.
Size
Document Number
97551 & FLASH
Rev
1A
Date: Friday, June 10, 2005
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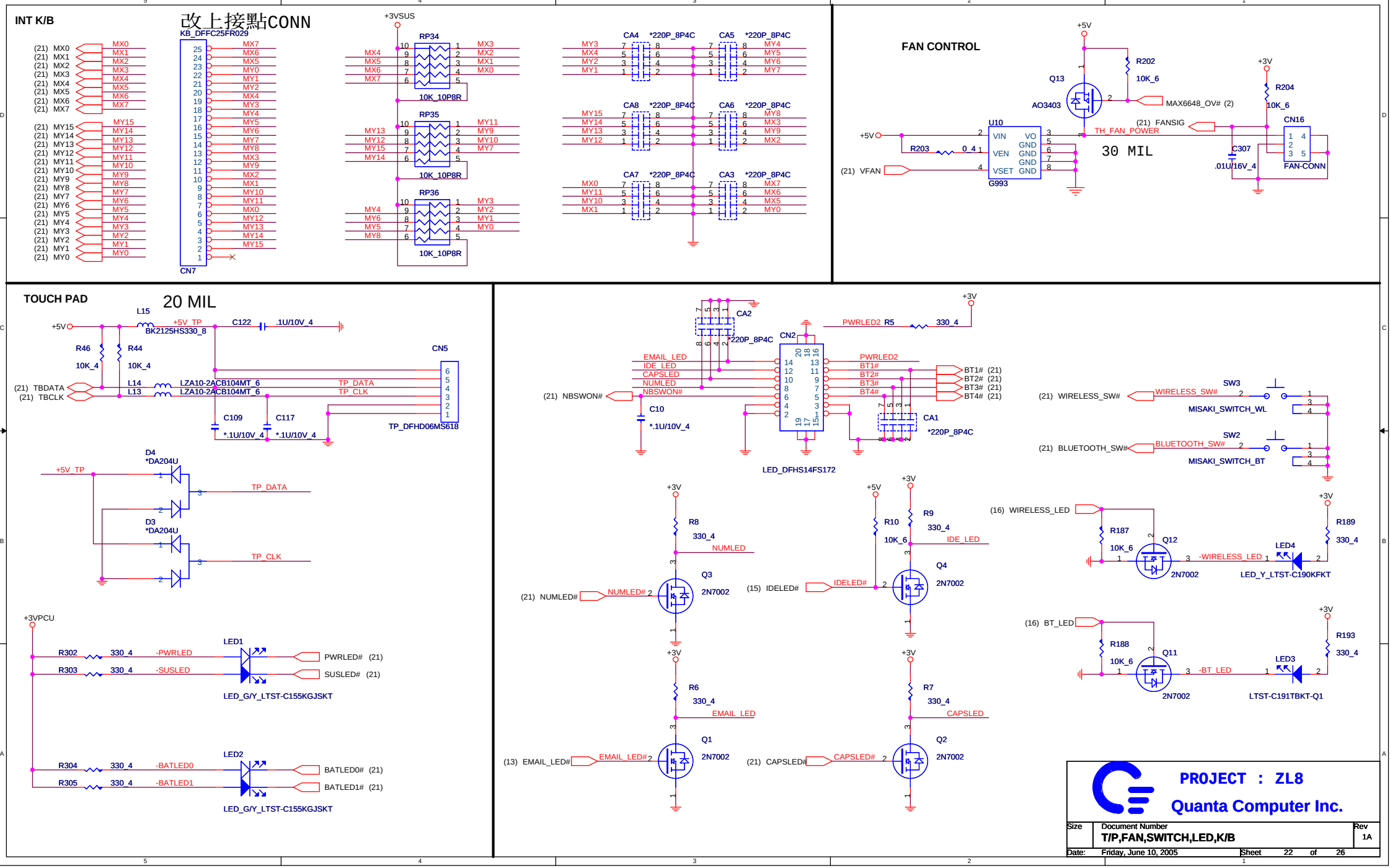
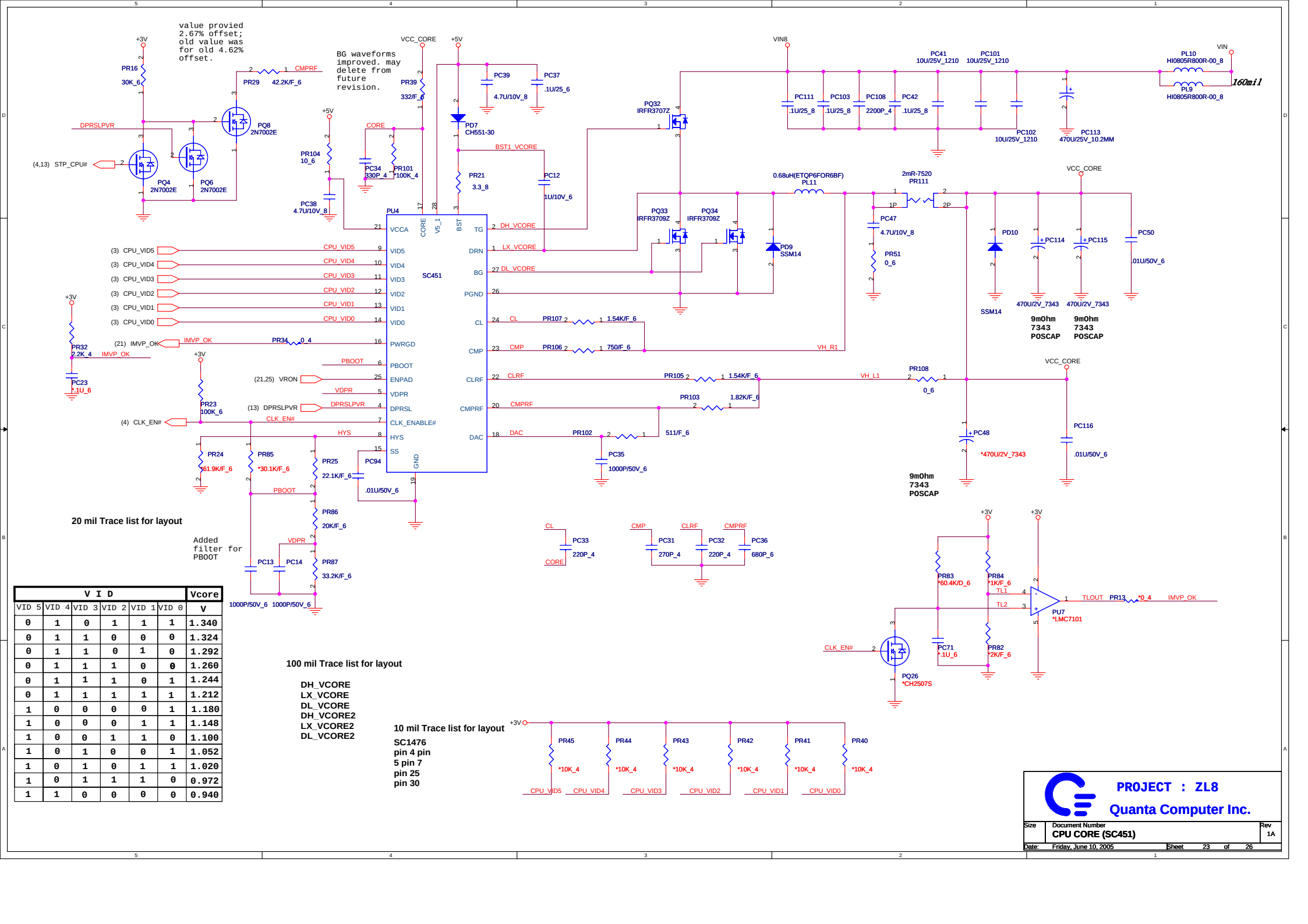
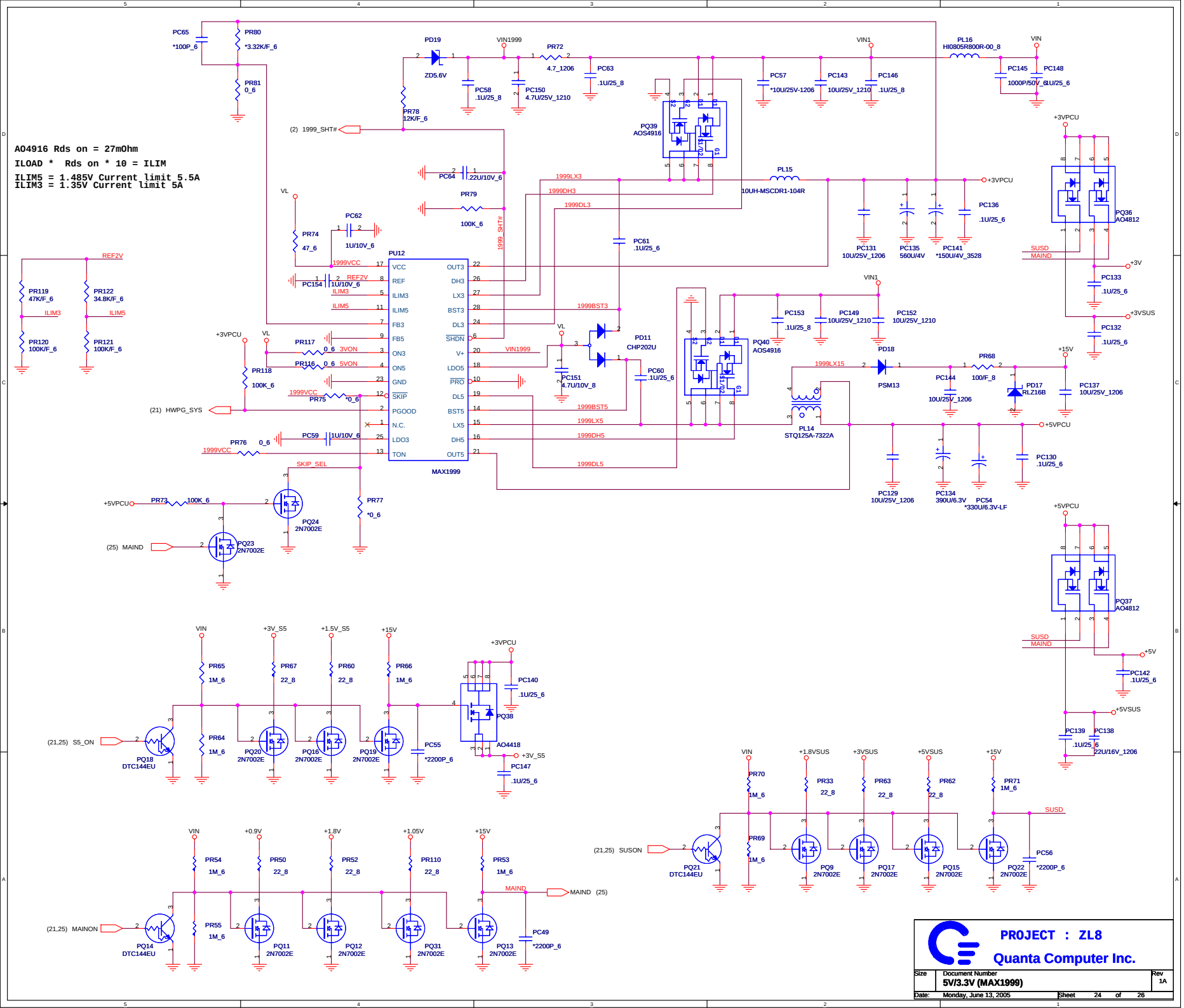

INT K/B
改上接點CONN
KB_DFFC25FR029
CN7
+3VSUS
RP34
RP35
RP36
10K_10P8R
CA4 *220P_8P4C
CA5 *220P_8P4C
CA8 *220P_8P4C
CA6 *220P_8P4C
CA7 *220P_8P4C
CA3 *220P_8P4C
FAN CONTROL
+5V
Q13
AO3403
R202
10K_6
MAX6648_OV# (2)
+3V
R204
10K_6
U10
VIN
VEN
VSET
VO
GND
G993
R203
0_4
(21) VFAN
(21) FANSIG
TH_FAN_POWER
30 MIL
CN16
FAN-CONN
C307
.01U/16V_4
TOUCH PAD
20 MIL
L15
BK2125HS330_8
+5V_TP
C122
.1U/10V_4
R46
10K_4
R44
10K_4
CN5
(21) TBDATA
(21) TBCLK
L14
L13
LZA10-2ACB104MT_6
TP_DATA
TP_CLK
C109
C117
*.1U/10V_4
TP_DFHD06MS618
D4
*DA204U
D3
*DA204U
+3VPCU
R302
330_4
-PWRLED
PWRLED# (21)
R303
330_4
-SUSLED
SUSLED# (21)
LED1
LED_G/Y_LTST-C155KGJSKT
R304
330_4
-BATLED0
BATLED0# (21)
R305
330_4
-BATLED1
BATLED1# (21)
LED2
LED_G/Y_LTST-C155KGJSKT
CA2
*220P_8P4C
CN2
EMAIL_LED
IDE_LED
CAPSLED
NUMLED
NBSWON#
(21) NBSWON#
C10
*.1U/10V_4
PWRLED2
R5
330_4
BT1# (21)
BT2# (21)
BT3# (21)
BT4# (21)
CA1
*220P_8P4C
LED_DFHS14FS172
R8
330_4
NUMLED
Q3
2N7002
(21) NUMLED#
R10
10K_6
R9
330_4
IDE_LED
Q4
2N7002
(15) IDELED#
R6
330_4
EMAIL_LED
Q1
2N7002
(13) EMAIL_LED#
R7
330_4
CAPSLED
Q2
2N7002
(21) CAPSLED#
(21) WIRELESS_SW#
SW3
MISAKI_SWITCH_WL
(21) BLUETOOTH_SW#
SW2
MISAKI_SWITCH_BT
(16) WIRELESS_LED
R187
10K_6
Q12
2N7002
-WIRELESS_LED
LED4
LED_Y_LTST-C190KFKT
R189
330_4
(16) BT_LED
R188
10K_6
Q11
2N7002
-BT_LED
LED3
LTST-C191TBKT-Q1
R193
330_4
PROJECT : ZL8
Quanta Computer Inc.
Size
Document Number
T/P,FAN,SWITCH,LED,K/B
Rev
1A
Date: Friday, June 10, 2005
Sheet 22 of 26




| V I D | | | | | | Vcore |
|---|---|---|---|---|---|---|
| VID 5 | VID 4 | VID 3 | VID 2 | VID 1 | VID 0 | V |
| 0 | 1 | 0 | 1 | 1 | 1 | 1.340 |
| 0 | 1 | 1 | 0 | 0 | 0 | 1.324 |
| 0 | 1 | 1 | 0 | 1 | 0 | 1.292 |
| 0 | 1 | 1 | 1 | 0 | 0 | 1.260 |
| 0 | 1 | 1 | 1 | 0 | 1 | 1.244 |
| 0 | 1 | 1 | 1 | 1 | 1 | 1.212 |
| 1 | 0 | 0 | 0 | 0 | 1 | 1.180 |
| 1 | 0 | 0 | 0 | 1 | 1 | 1.148 |
| 1 | 0 | 0 | 1 | 1 | 0 | 1.100 |
| 1 | 0 | 1 | 0 | 0 | 1 | 1.052 |
| 1 | 0 | 1 | 0 | 1 | 1 | 1.020 |
| 1 | 0 | 1 | 1 | 1 | 0 | 0.972 |
| 1 | 1 | 0 | 0 | 0 | 0 | 0.940 |


AO4916 Rds on = 27mOhm
ILOAD * Rds on * 10 = ILIM
ILIM5 = 1.485V Current limit 5.5A
ILIM3 = 1.35V Current limit 5A
PROJECT : ZL8
Quanta Computer Inc.
Document Number
5V/3.3V (MAX1999)
Rev
1A
Size
Date: Monday, June 13, 2005
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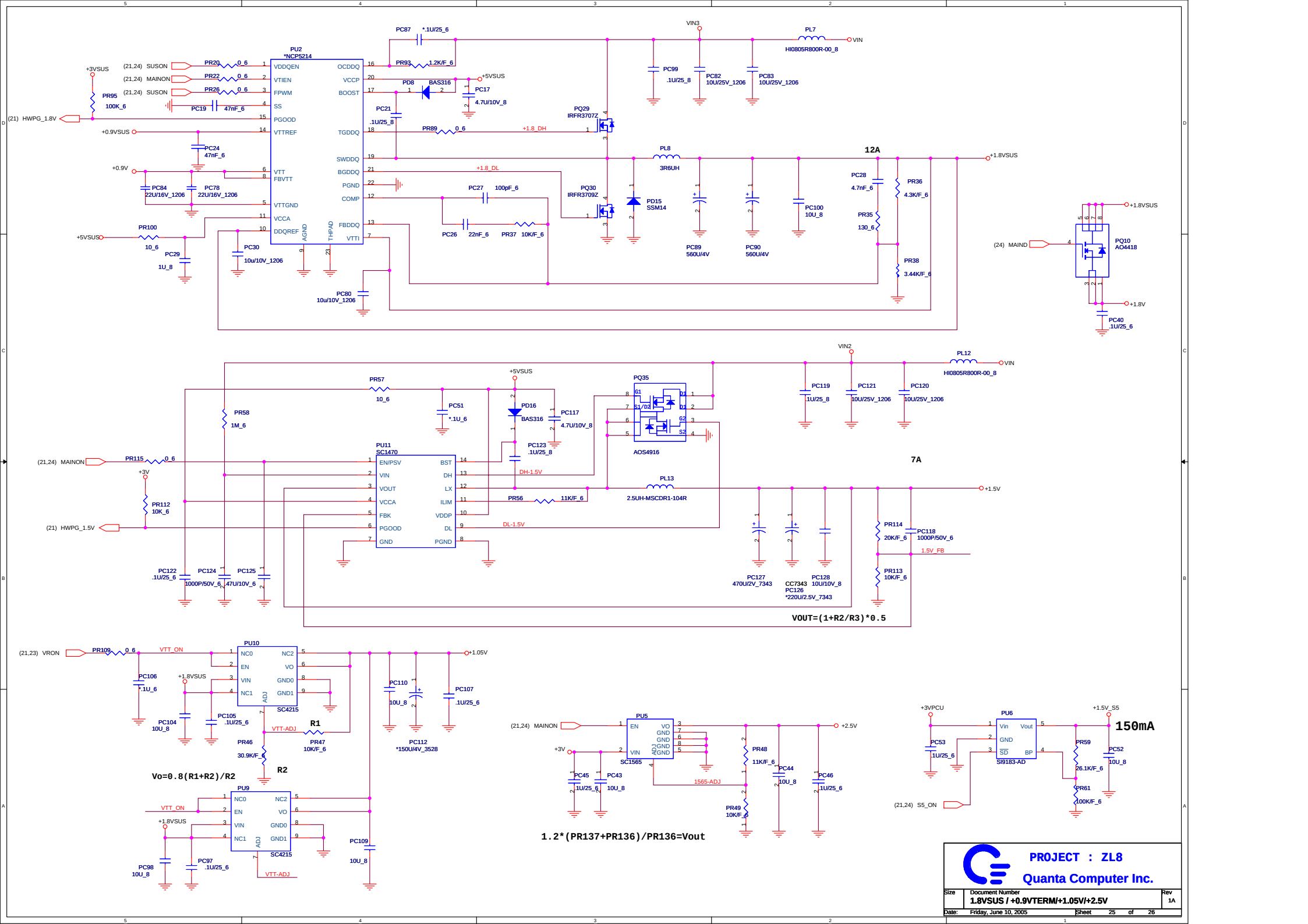
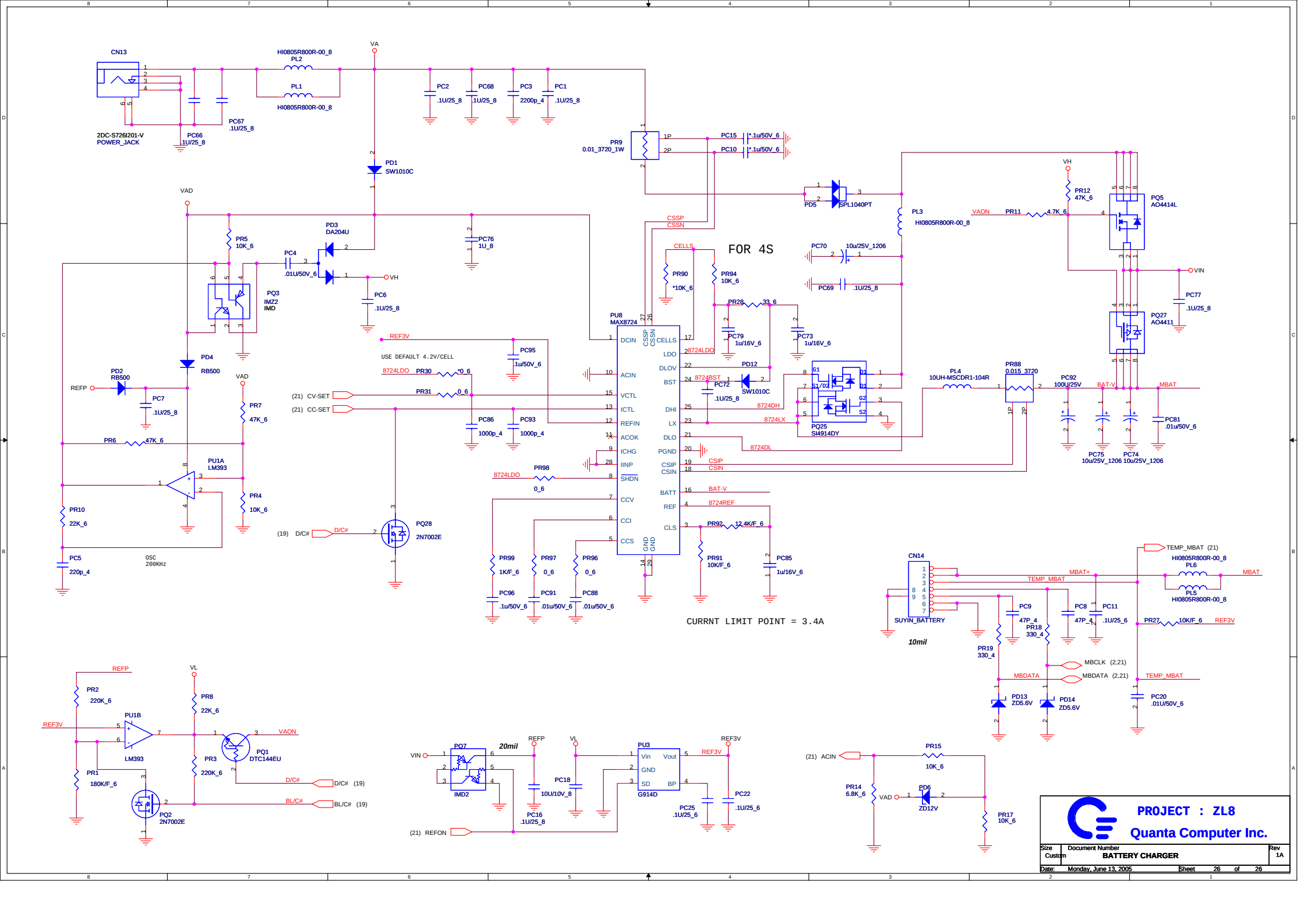



PROJECT : ZL8

Quanta Computer Inc.

| Size | Document Number | Rev |
|---|---|---|
| | 1.8VSUS / +0.9VTERM/+1.05V/+2.5V | 1A |
| Date: Friday, June 10, 2005 | Sheet 25 of 26 | |


CN13
2DC-S726I201-V
POWER_JACK
PC67 .1U/25_8
PC66 .1U/25_8
HI0805R800R-00_8
PL2
PL1
HI0805R800R-00_8
VA
PC2 .1U/25_8
PC68 .1U/25_8
PC3 2200p_4
PC1 .1U/25_8
PD1 SW1010C
PR9 0.01_3720_1W
PC15 *.1u/50V_6
PC10 *.1u/50V_6
PD5 SPL1040PT
PL3 HI0805R800R-00_8
VH
PR12 47K_6
PQ5 AO4414L
VAON
PR11 4.7K_6
VIN
PC77 .1U/25_8
PQ27 AO4411
VAD
PR5 10K_6
PD3 DA204U
PC4 .01U/50V_6
PC6 .1U/25_8
PC76 1U_8
PQ3 IMZ2 IMD
PD4 RB500
PD2 RB500
REFP
PC7 .1U/25_8
PR6 47K_6
PR7 47K_6
PR4 10K_6
PR10 22K_6
PC5 220p_4
PU1A LM393
OSC 200KHz
REF3V
USE DEFAULT 4.2V/CELL
8724LDO
PR30 *0_6
PR31 0_6
(21) CV-SET
(21) CC-SET
PC95 .1u/50V_6
PC86 1000p_4
PC93 1000p_4
PQ28 2N7002E
(19) D/C#
PU8 MAX8724
DCIN
ACIN
VCTL
ICTL
REFIN
ACOK
ICHG
IINP
SHDN
CCV
CCI
CCS
CSSP
CSSN
CELLS
LDO
DLOV
BST
DHI
LX
DLO
PGND
CSIP
CSIN
BATT
REF
CLS
GND
FOR 4S
PR90 *10K_6
PR94 10K_6
PR28 33_6
PC79 1u/16V_6
PC73 1u/16V_6
PC72 .1U/25_8
PD12 SW1010C
8724BST
8724DH
8724LX
8724DL
PQ25 SI4914DY
PC70 10u/25V_1206
PC69 .1U/25_8
PR88 0.015_3720
PL4 10UH-MSCDR1-104R
PC92 100U/25V
PC75 10u/25V_1206
PC74 10u/25V_1206
PC81 .01u/50V_6
BAT-V
MBAT
PR98 0_6
PR99 1K/F_6
PR97 0_6
PR96 0_6
PC96 .1u/50V_6
PC91 .01u/50V_6
PC88 .01u/50V_6
PR92 12.4K/F_6
PR91 10K/F_6
PC85 1u/16V_6
8724REF
CURRNT LIMIT POINT = 3.4A
CN14
SUYIN_BATTERY
10mil
TEMP_MBAT (21)
HI0805R800R-00_8
PL6
PL5
HI0805R800R-00_8
MBAT+
TEMP_MBAT
PC9 47P_4
PC8 47P_4
PC11 .1U/25_6
PR27 10K/F_6
REF3V
PR18 330_4
PR19 330_4
MBCLK (2,21)
MBDATA
MBDATA (2,21)
TEMP_MBAT
PD13 ZD5.6V
PD14 ZD5.6V
PC20 .01U/50V_6
REFP
VL
PR2 220K_6
PR8 22K_6
PU1B LM393
PR1 180K/F_6
PR3 220K_6
PQ1 DTC144EU
VAON
D/C#
D/C# (19)
BL/C#
BL/C# (19)
PQ2 2N7002E
PQ7 IMD2
20mil
VIN
REFP
VL
PC18 10U/10V_8
PC16 .1U/25_8
(21) REFON
PU3 G914D
Vin
Vout
GND
SD
BP
REF3V
PC25 .1U/25_6
PC22 .1U/25_6
(21) ACIN
PR15 10K_6
PR14 6.8K_6
PD6 ZD12V
PR17 10K_6
PROJECT : ZL8
Quanta Computer Inc.
Size Custom
Document Number BATTERY CHARGER
Rev 1A
Date: Monday, June 13, 2005
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